



# How Innovative Is China in Semiconductors?

STEPHEN EZELL | AUGUST 2024

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Chinese competitors stand about five years behind global leaders in high-volume manufacturing of leading-edge logic semiconductor chips and continue to trail in memory chips and semiconductor manufacturing equipment, although Chinese firms have made inroads in semiconductor design and production of legacy semiconductor chips.

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## KEY TAKEAWAYS

- China seeks to achieve self-sufficiency in all facets of the semiconductor industry, simultaneously reducing its reliance on foreign competitors while attempting to build competitive enterprises.
- Chinese “catch up” across semiconductor subsectors is uneven: In the design of logic chips, such as for mobile devices or artificial intelligence (AI) applications, Chinese enterprises remain behind global leaders, though perhaps only by two years.
- However, in other subsectors of the semiconductor industry, notably in memory chips, semiconductor manufacturing equipment (SME), and assembly, test, and packing (ATP), Chinese enterprises are innovating but several years more behind global leaders.
- In 2021–2022, 55 percent of global semiconductor patent applications were Chinese in origin (and China’s number of applications doubled America’s) while Chinese entities surpassed U.S. and Japanese ones for semiconductor patents granted in 2022.
- However, in terms of semiconductor industry research and development (R&D) intensity, China’s rate of 7.6 percent was just 40 percent of America’s (18.8 percent), and below European Union firms’ 15 percent.
- China’s intense efforts to develop a fully fledged, indigenous, “closed loop” semiconductor industry have spurred a great deal of ingenuity and innovation, but a “go-it-alone” strategy will be very difficult in such an extremely complex tech ecosystem, especially in the face of semiconductor export controls.

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## INTRODUCTION

Semiconductors represent one of the world's most important industries, the core technology that powers the modern digital world.<sup>1</sup> Recognizing this vital role, China's government has prioritized the sector, investing hundreds of billions of dollars to catalyze the development of an indigenous semiconductor ecosystem and to ideally cultivate globally competitive semiconductor firms across virtually all segments of the semiconductor value chain, from semiconductor design and fabrication to assembly, test, and packaging (ATP).

Thus far, those efforts have met with uneven success. With regard to the fabrication of leading-edge logic semiconductor chips, China's flagship competitor, the Semiconductor Manufacturing International Corporation (SMIC), likely stands about five years behind global leaders such as the Taiwan Semiconductor Manufacturing Company (TSMC).<sup>2</sup> As G. Dan Hutcheson, vice chair of research firm TechInsights, explained, "Ten years ago, [Chinese companies] were two generations behind. Five years ago, they were two generations behind, and now they're still two generations behind."<sup>3</sup> Chinese competitors are even further behind with regard to semiconductor manufacturing equipment (SME), such as the lithography tools that make semiconductors: One commentator noted that Chinese firms might be as many as five generations behind in this field.<sup>4</sup> As one analyst explained, "The best machinery a Chinese company can produce makes chips that are 28 nanometers wide; the industry's cutting-edge equipment can make 2-nanometer chips."<sup>5</sup>

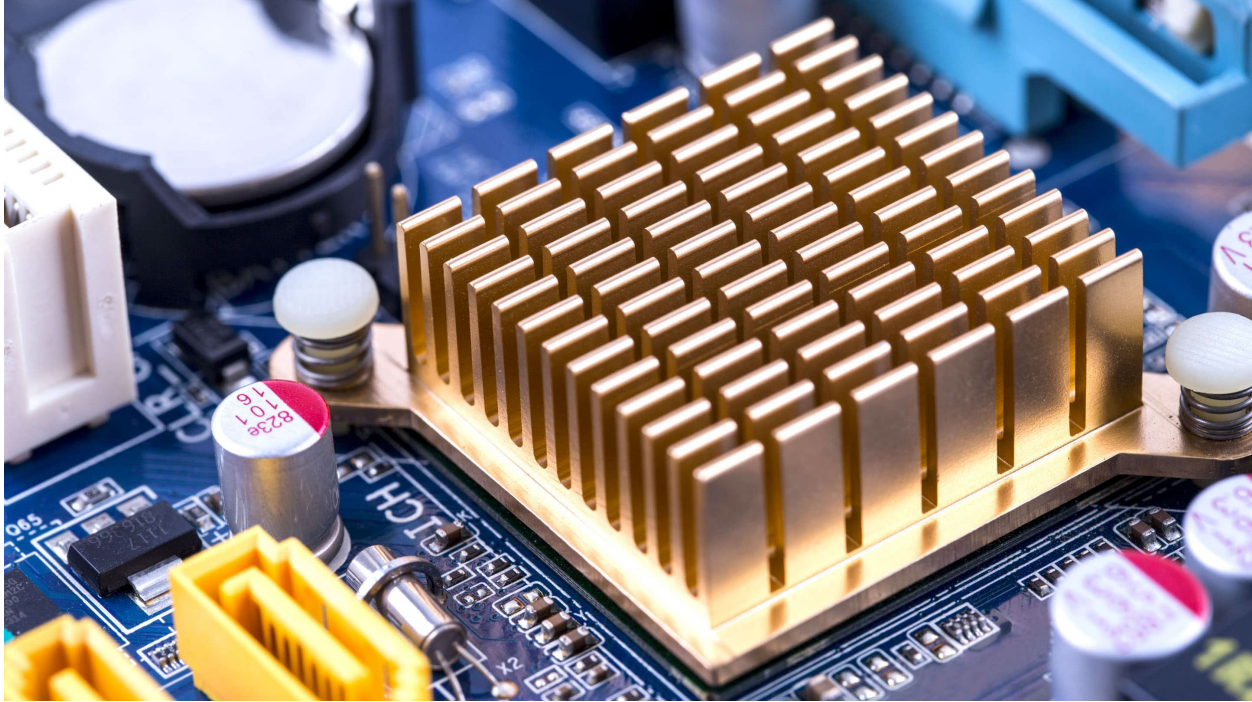
That said, Chinese semiconductor firms appear to be catching up in certain pockets: for instance, industry analysts view the design attributes and features of Huawei's Mate 60 Pro smartphone as within 18 to 24 months of competitors' versions. China has also made inroads in the production of legacy semiconductors (those greater than 28 nanometers), although Chinese firms appear to be competing in this sector on a more price- than innovation-intensive basis. In total, China continues to lag behind global leaders in most facets of semiconductor design and fabrication, but its firms' intellectual property (IP) and innovation capabilities are accelerating rapidly as China pursues an aggressive whole-of-society strategy in an intense state-directed effort to achieve domestic semiconductor self-sufficiency.

## A BRIEF OVERVIEW OF THE GLOBAL SEMICONDUCTOR INDUSTRY

Modern semiconductors contain billions of transistors on a chip the size of a square centimeter, with circuits measured at the nanoscale ("nm," a unit of length equal to one billionth of a meter). The very newest semiconductor fabrication facilities, which can cost over \$30 billion to construct, produce semiconductors at 3 or 2 nm (and even sub-2 nm) scales.<sup>6</sup>

The semiconductor sector is a \$527 billion global industry that's expected to become a trillion-dollar one by 2030.<sup>7</sup> Over 70 new semiconductor fabs are expected to be constructed worldwide by 2030 to satisfy this growing demand.<sup>8</sup> In short, semiconductors represent the commanding heights of the modern global digital economy, and this explains why leadership in the sector is so fiercely contested among nations, not least the People's Republic of China (PRC), the European Union nations, Japan, South Korea, Taiwan, and the United States.

**Computer circuit board technology (with processor), close up. Modern semiconductors contain billions of transistors on a chip the size of a square centimeter, with circuits measured at the nanoscale.**



The semiconductor production process represents perhaps the most complex engineering task humanity undertakes. When all production phases are considered, the entire semiconductor production process extends from raw material procurement to end-product manufacturing.<sup>9</sup> (See figure 1.) The core (or “narrow”) steps of the semiconductor production process include chip design, chip fabrication, and back-end ATP, with these steps supported by key inputs such as electronic design automation (EDA) software and SME such as lithography, deposition, and etch tooling.

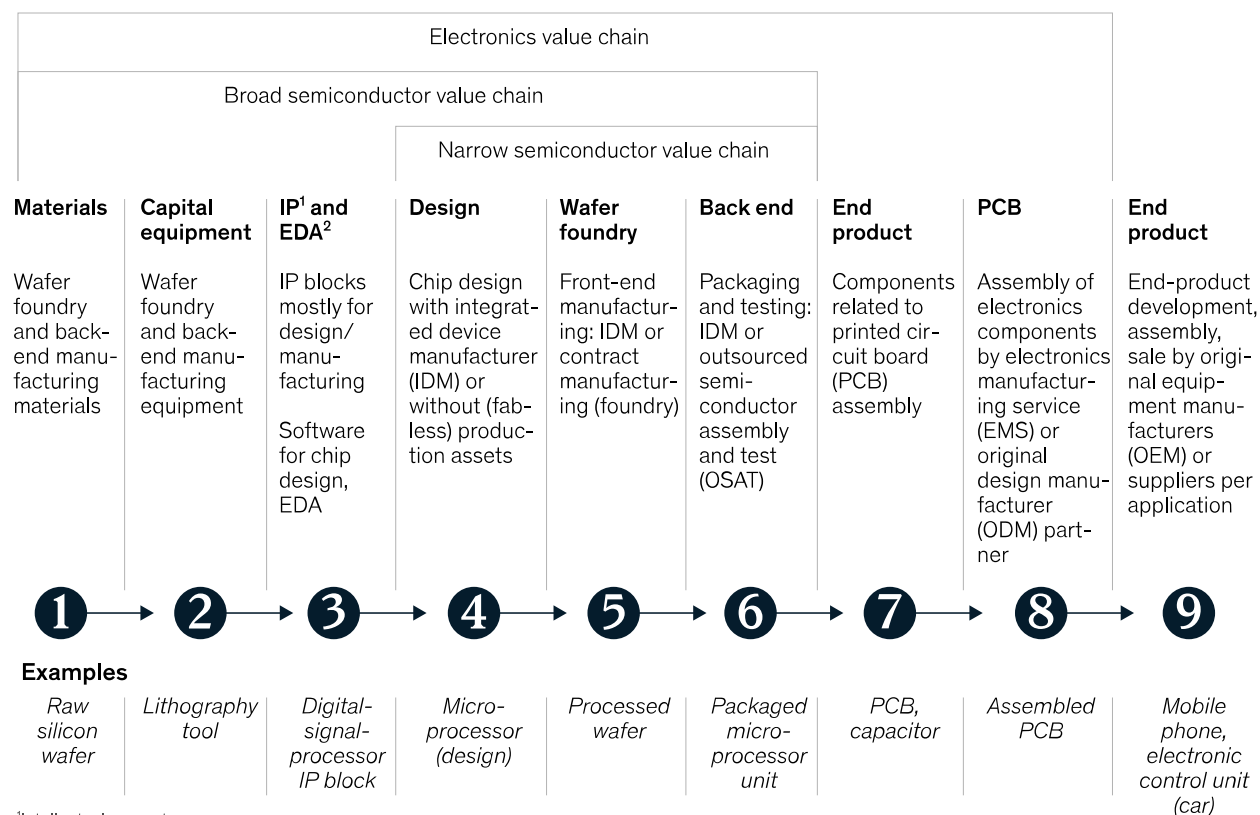
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**Semiconductors represent one of the world’s most important industries, the core technology powering the modern digital world and empowering innovation and productivity growth across every industry.**

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Lithography, wherein a chip wafer gets inserted into a lithography machine and exposed to deep ultraviolet (DUV) or extreme ultraviolet (EUV) light and a pattern is printed onto a chip’s resist layer through a photomask, in particular represents a crucial step in the chipmaking process. EUV represents the latest and most sophisticated lithography technology, which Dutch firm ASML having invested €6 billion (\$6.5 billion) to innovate over the past 17 years.<sup>10</sup> The October 2022 export controls the United States placed on China included restrictions on equipment that can manufacture chips below 20 nm (impacting both DUV and EUV).<sup>11</sup> In June 2023, the United States brokered a deal with the Netherlands to restrict exports of leading-edge EUV equipment to China.<sup>12</sup>

**Figure 1: Facets of the semiconductor value chain<sup>13</sup>**



<sup>1</sup>Intellectual property.  
<sup>2</sup>Electronic-design automation.

The four most prevalent types of semiconductors are logic chips, memory chips (usually dynamic random-access memory (DRAM) or NAND “flash”), analog chips (those which generate a signal or transform signal characteristics, and are especially prevalent in automotive and audio applications), and power chips (those used as a switch in power electronics). “Advanced” or leading-edge logic chips are generally viewed as being sub-14 nm, while “legacy” (often called “mature-node”) chips refer to those manufactured using 28 nm or larger technology processes. Legacy chips are especially common in automobiles, medical devices, household appliances, energy, infrastructure, and aerospace products.

Lastly, several key business models define the industry. Integrated device manufacturers (IDMs) characterize firms—such as Infineon, Intel, Micron, Renesas, Samsung, SK Hynix, and Texas Instruments—which conduct all key facets of semiconductor manufacturing, especially design and fabrication, internally. Foundries such as Global Foundries, SMIC, and TSMC specialize solely in semiconductor manufacturing, often of the chip designs developed by “fabless” companies that specialize in designing application-specific chips, such as Advanced Micro Devices (AMD), chips for AI, high-performance computing (HPC), and graphics, Apple (mobile devices), NVIDIA (AI chips), or Qualcomm (5G and other wireless chips).<sup>14</sup>



## BACKGROUND AND METHODOLOGY

The common narrative is that China is a copier and the United States is an innovator. That narrative often supports a lackadaisical attitude toward U.S. technology and industrial policy. After all, America leads (almost by right) in innovation, so there is nothing to worry about. But, first, this assumption is misguided because it is possible for innovators to lose leadership to copiers with lower cost structures, as has been the case in many U.S. industries, including consumer electronics, solar panels, telecom equipment, and machine tools.<sup>15</sup> Second, it's not clear that China is a sluggish copier that's always destined to be a follower.

To assess how innovative Chinese industries are, the Smith Richardson Foundation provided support to the Information Technology and Innovation Foundation (ITIF) to research this question. As part of this research, ITIF is reviewing particular sectors, including semiconductors.

To be sure, it is difficult to assess the innovation capabilities of any country's industries, but it is especially difficult for Chinese industries. In part, this is because, under President Xi Jinping, China discloses much less information to the world than it used to, especially about its industrial and technological capabilities. Indeed, as *The Economist* wrote, "China's chip industry operates under a shroud of secrecy. Breakthroughs and setbacks are often deemed to be state secrets, the divulgence of which can result in arrest."<sup>16</sup>

Notwithstanding this, ITIF relied on three methods to assess Chinese innovation in semiconductors. First, we conducted in-depth case study evaluations of several Chinese semiconductor companies randomly selected from companies listed on the "2023 EU Industrial R&D Investment Scoreboard." Second, we conducted interviews and held a focus group roundtable with global experts on the Chinese semiconductor industry. And, third, we assessed global data on semiconductor innovation, including scientific articles and patents. This report provides ITIF's assessment of China's current level of innovativeness in the semiconductor sector and offers a forward-looking perspective based upon rapidly evolving trendlines.

## IMPORTANCE OF SEMICONDUCTORS AND THE U.S. ROLE

The United States invented the semiconductor industry, harkening back to 1947, when Bell Labs' John Bardeen, Walter Brattain, and William Shockley invented the transistor, a semiconductor device used to amplify or switch electronic signals and electrical power. In the mid-1950s, Jack Kilby at Texas Instruments and Robert Noyce and a team of researchers at Fairchild Semiconductor pioneered the integrated circuit (IC), placing multiple transistors on a single flat piece of semiconductor material, giving rise to the modern visage of a "semiconductor chip."<sup>17</sup>

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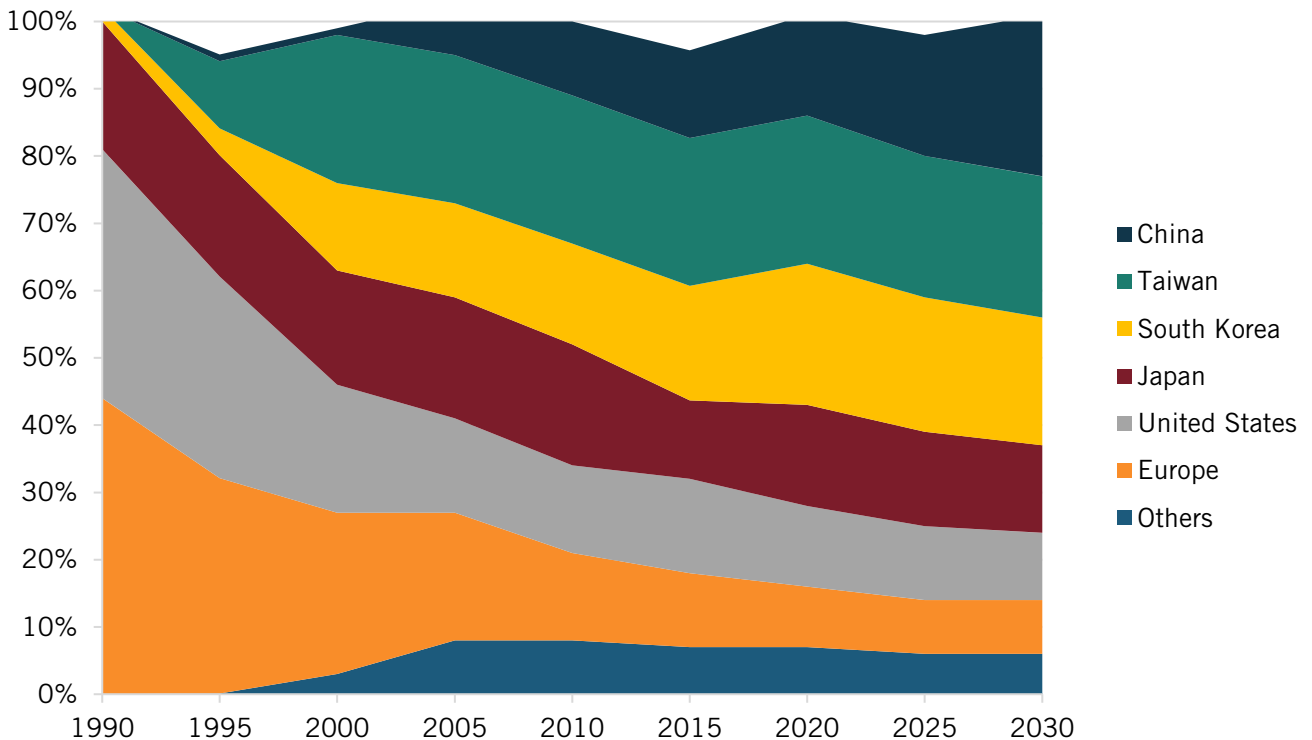
**From 1990 to 2021, the U.S. share of global semiconductor production fell by 70 percent, from 37 percent to 12 percent.**

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U.S. enterprises continue to lead the world in semiconductor design (e.g., AMD, Apple, Qualcomm, etc.), and are highly competitive in semiconductor manufacturing (e.g., Intel and Micron) and SME tooling (e.g., Applied Materials, Lam Research, etc.) Indeed, as the Australian Strategic Policy Institute (ASPI) wrote, "The US excels in the design and development of the most advanced semiconductor chips and has a research lead in the technology areas of *high*

*performance computing and advanced integrated circuit design and fabrication.*”<sup>18</sup> However, while the emergence of the fabless chip design ecosystem has preserved America’s leading revenue share in the global semiconductor industry, where the United States has severely faltered is in its extent of semiconductor manufacturing. In fact, from 1990 to 2021, the U.S. share of global semiconductor production fell by 70 percent, from 37 percent to 12 percent.<sup>19</sup> Conversely, over that time, China’s share rose from virtually nil to 12 percent. (See figure 2.)

**Figure 2: Global semiconductor manufacturing capacity, 1990–2030 forecast<sup>20</sup>**



The significantly declining U.S. share of global semiconductor manufacturing activity was the catalyst for America’s major recommitment to revitalizing the competitiveness of its semiconductor industry when Congress passed, and the Biden administration signed, the 2022 CHIPS and Science Act. The legislation authorized a 25 percent investment tax credit (ITC) and appropriated \$52.7 billion to support the industry, including \$11 billion for R&D activities and \$39 billion for a “CHIPS for America Fund” to bolster U.S. semiconductor manufacturing by providing financial incentives for building, expanding, and equipping domestic fabrication facilities.<sup>21</sup> The United States (like Europe) seeks to double its share of global semiconductor manufacturing activity over the coming decade—objectives that will abut China’s efforts to also similarly increase its share in the years ahead.<sup>22</sup> That U.S. semiconductor companies depend on the Chinese market for 36 percent of their sales represents a significant concern as China seeks to grow its domestic industry in a quest for autarkic self-sufficiency.<sup>23</sup>

## OVERVIEW OF CHINA’S SEMICONDUCTOR INDUSTRY

As noted, China lags behind the global innovation frontier in most aspects of semiconductor design and fabrication, including with regard to key inputs such as EDA software and lithography equipment. However, Chinese enterprises such as Huawei’s HiSilicon and Biren have innovated

increasingly competitive logic chips, especially those powering mobile devices and graphical processing units (GPUs) servicing AI applications. China has also demonstrated an ability to cost-effectively manufacture less-technologically complex mature-node logic chips. Chinese memory chip manufacturers Yangtze Memory Technologies Co. (YMTC) and ChangXin Memory Technologies (CXMT) seemed to be making significant strides up until about 2020, but appear to have fallen off the innovation pace relative to global leaders since.

Nevertheless, China is rapidly closing the gap across many facets of the semiconductor production process and is developing genuine IP and innovation capabilities across the board. In January 2024, Intel CEO Pat Gelsinger asserted that, despite China’s ongoing efforts to advance its semiconductor industry and design more sophisticated chip manufacturing tools, the country still lags behind the global semiconductor industry by approximately 10 years.<sup>24</sup> While there’s no question that China’s behind, the real gap, as noted, is probably half that now, or about five years—at least for the design and fabrication of leading-edge logic chips. China continues to plough hundreds of billions of dollars into its semiconductor industry in an effort to close that gap. Moreover, over the long term, as one observer commented, “The likelihood of China developing advanced chip-making capabilities is almost certain.”<sup>25</sup>

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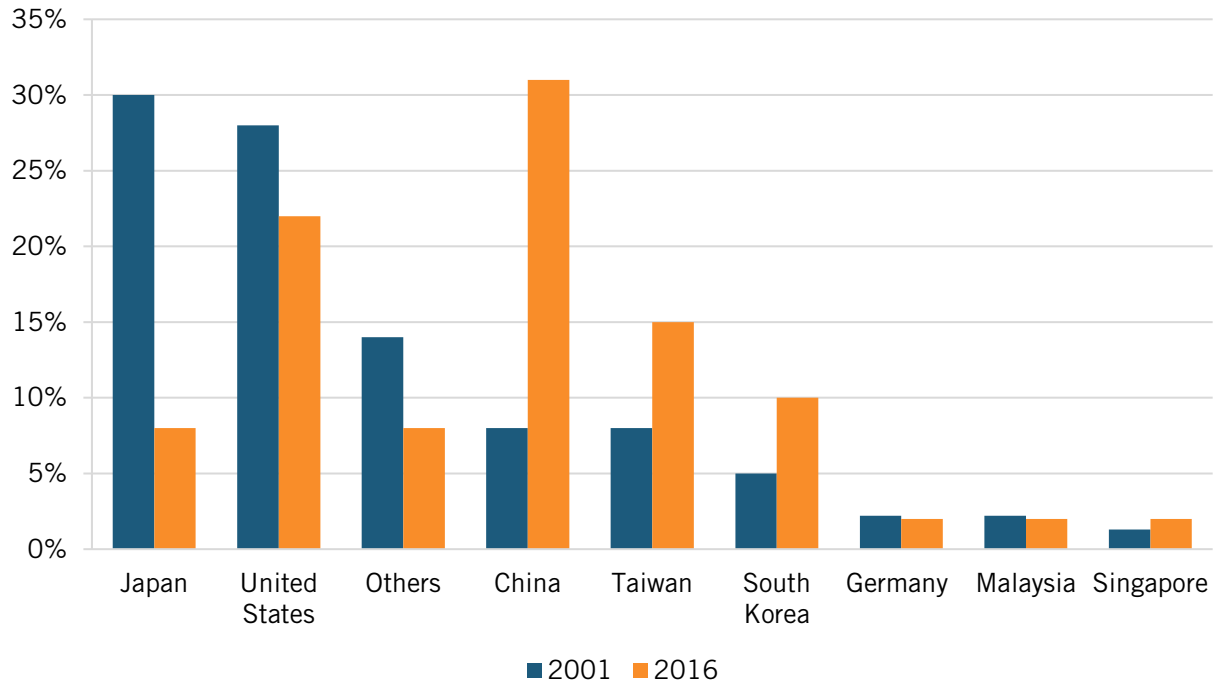
Ever since the 2013 Third Party Plenum, the Chinese government “has made semiconductors the country’s top industrial innovation priority.”<sup>26</sup> China’s “2014 National Guidelines for Development and Promotion of the IC Industry” (often called the “National IC Strategy”) called for \$150 billion in investments to establish a fully “closed-loop” semiconductor ecosystem in China. The plan unabashedly called for eliminating China’s trade deficit in ICs by 2030 and making China the world’s leader in IC manufacturing by then.<sup>27</sup> In 2015, China released its “Made in China 2025” (MIC 2025) strategy, which refined some of these targets, setting a goal of achieving 40 percent self-sufficiency in semiconductors by 2020 and 70 percent by 2025.<sup>28</sup> In reality, China is likely to only achieve 30 percent self-sufficiency by the end of 2025.<sup>29</sup> RAND’s Jimmy Goodrich has noted that China would probably need to invest at least an additional \$1 trillion more to achieve true self-sufficiency in the industry.<sup>30</sup>

Nevertheless, these strategies make clear that China recognizes that semiconductors represent a foundational technology that underpins its economic and national security wherewithal, and that the country is willing to undertake the long-term investments necessary to reach its goal of achieving self-sufficiency and reducing dependence on foreign technologies in this critical sector. As Goodrich explained, “Semiconductors are integral to China’s goal of developing a complete industrial system. This means having capability up and down the supply chain, with the goal that, in the event of a conflict, China can sustain its industrial system and its economy.”<sup>31</sup>

China’s efforts have paid dividends. For instance, from 2001 to 2016, China’s share of global value added in the semiconductor industry grew almost fourfold, from 8 to 31 percent, while the United States’ share fell from 28 to 22 percent, and Japan’s share fell by over two-thirds, from 30 to 8 percent. (See figure 3.)

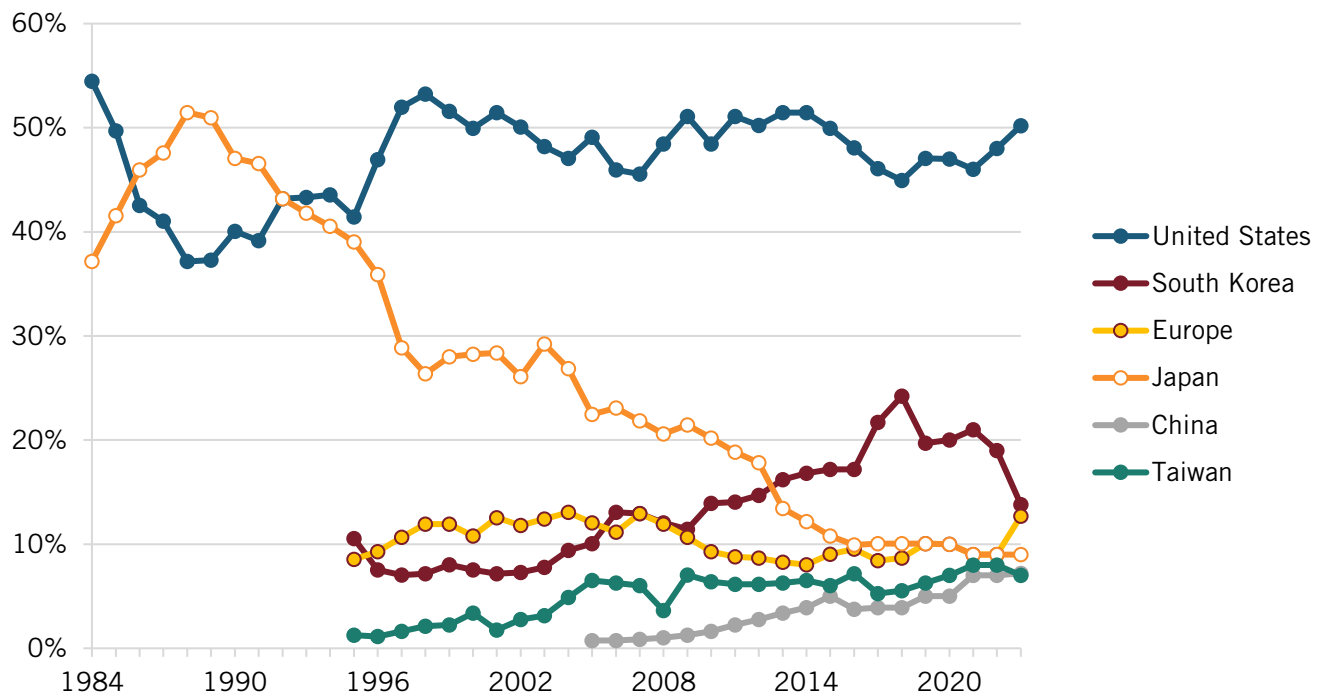


**Figure 3: Country share of value added in global semiconductor industry<sup>32</sup>**



Nevertheless, according to data from the Semiconductor Industry Association (SIA), considering global semiconductor industry market share, China commanded only 7.2 percent of the global market in 2023, compared with 7 percent for Taiwan, 9 percent for Japan, 12.7 percent for the EU, 18.8 percent for South Korea, and 50.2 percent for the United States. (See figure 4.)

**Figure 4: Global semiconductor industry sales market share by nationally headquartered companies<sup>33</sup>**



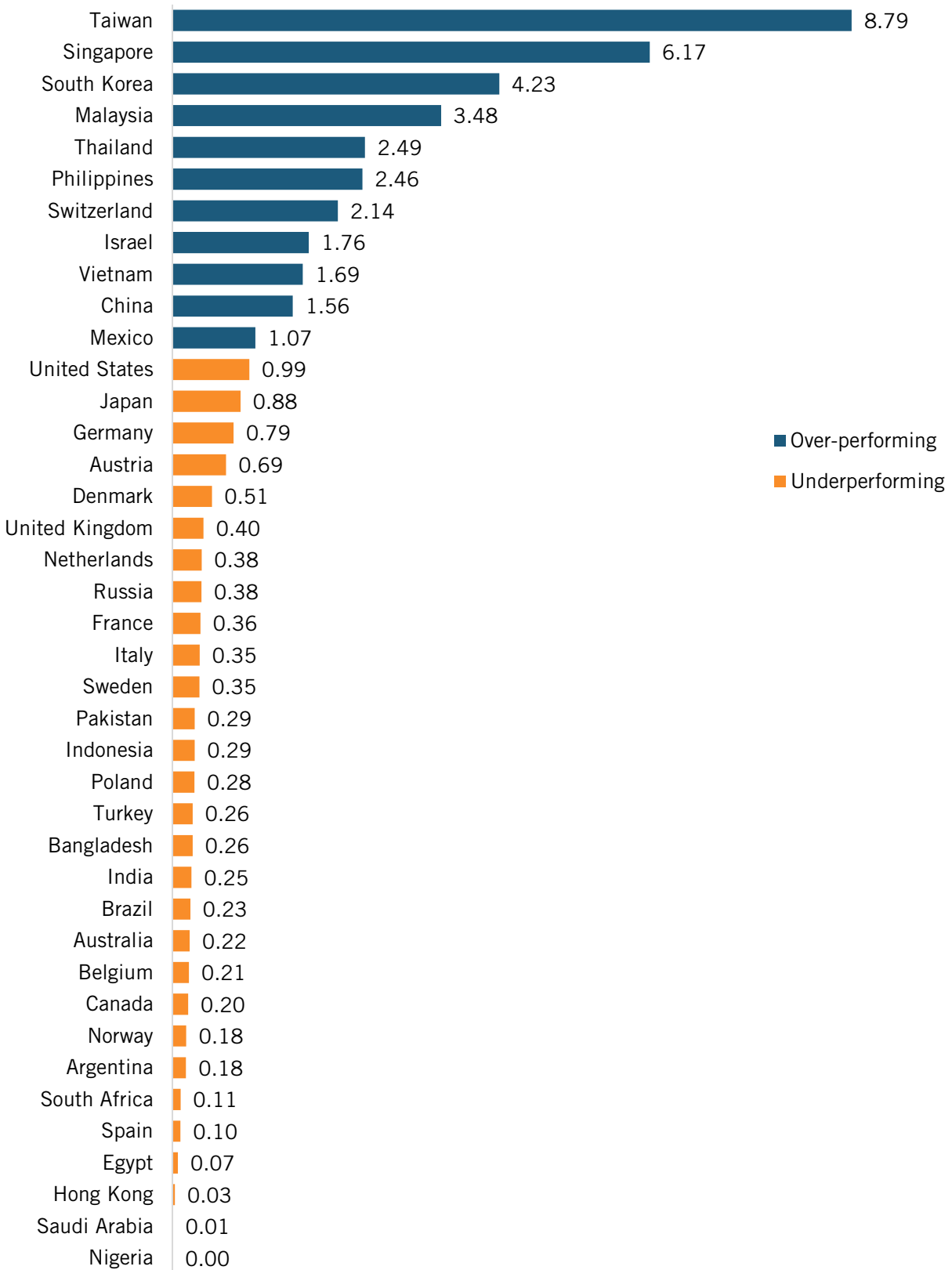
Despite the significant decline in the share of global semiconductor manufacturing the United States has experienced, it has maintained its leadership in overall global semiconductor market share thanks especially to its strengths in EDA and semiconductor design as well as to world-leading companies in the logic, memory, and analog market segments.

Japan, once the world leader in semiconductors, has experienced significant decline over the past three decades. Since 1988, Japan has gone from holding 52 percent of the semiconductor market share to just 7 percent, a decrease of 87 percent. This major decline can be attributed to an inability or unwillingness of Japanese firms to adapt during the 1990s and early 2000s as other significant semiconductor foundries were established. This lack of innovation, research, and development, coupled with falling Japanese electronics sales, made adaptation and competition in the semiconductor industry difficult for legacy Japanese companies.<sup>34</sup>

One of these new semiconductor manufacturers founded during the 1990s was TSMC. Now the largest manufacturer of semiconductors in the world, TSMC produces the majority of semiconductors made in Taiwan. The semiconductor industry accounts for 15 percent of Taiwan's gross domestic product (GDP) while producing 60 percent of the world's semiconductor chips, a large percentage of which are highly advanced.<sup>35</sup> Though Taiwan accounts for just a small share of the global market (7 percent), its output relative to its size makes Taiwan the strongest relative performer. This can be seen using location quotient (LQ) information from ITIF's Hamilton Index, which assessed 10 strategically important industries. An LQ is calculated as a country's share of global output in an industry divided by the country's overall share of the global economy.<sup>36</sup>

Figure 5 shows LQ data for the Hamilton Index category of the computers and electronics industry, a strategically important industry that includes semiconductors. Taiwan tops the list with an LQ of 8.79, exceeding close competitors such as South Korea and Singapore with LQs of 6.17 and 4.23, respectively. With LQs greater than 1, all three countries are overperforming, producing an output in the computer and electronics industry greater than the global average. China's LQ of 1.56 means that the computers and electronics industry contributes 50 percent more to China's economy than it contributes to the global economy. The United States' LQ of 0.99 means that the computers and electronics industry contributes about as much to the U.S. economy as it contributes relatively to the global economy. Japan lags behind here with an LQ of 0.88. (See figure 5.)

**Figure 5: Relative performance in computers and electronics industries<sup>37</sup>**



## SUBSECTOR ANALYSIS OF CHINA'S SEMICONDUCTOR INDUSTRY

This section analyzes China's innovativeness by industry subsector, considering China's strengths in EDA, semiconductor design and fabrication—of leading-edge and mature node logic chips, and memory chips—as well as the semiconductor ATP and SME/tooling sectors.

### Electronic Design Automation

EDA refers to the software, hardware, and services that assist in the definition, planning, design, implementation, verification, and subsequent manufacturing of semiconductors.<sup>38</sup> Historically, EDA has “constitute[d] a weak link in the PRC's efforts to build a leading domestic semiconductor value chain.”<sup>39</sup> As such, China has targeted progress in EDA since MIC 2025 and the 14th Five Year Plan, with the efforts now “logging some initial successes.”<sup>40</sup>

In March 2023, Huawei announced that it had achieved a number of breakthroughs in the development of EDA software, which it suggested would free China's industry from reliance on foreign suppliers of those tools when producing semiconductors of 14 nm or more.<sup>41</sup> Huawei claimed it had substituted 78 (foreign) software and hardware items over the previous three years.<sup>42</sup> These breakthroughs likely came in partnership with Beijing-based Empyrean Technology, China's leading EDA player. Empyrean claimed last year that it could fully support 7 nm digital processes and 5 nm analog processes.<sup>43</sup>

Beyond Empyrean, other leading Chinese EDA companies include GWX Technology, Primarius, Semitronix, Shenzhen Giga Design Automation, UniVista, and X-EPIC.<sup>44</sup> By 2020, Chinese companies had captured 11.5 percent of the domestic EDA market (up from 6.2 percent in 2018), with that share expected to increase to 14 percent by 2025.<sup>45</sup>

In June 2023, in Nanjing, China, Southeast University launched a National EDA Innovation Center, the first national technology innovation center in China dedicated to IC design (and which had been under development since September 2020). Yang Jun, a professor at Southeast University tasked with leading the center, noted its mission would be to “undertake the critical work of breaking the United States' chokehold in EDA software,” not just for mature-node chips, but also ultimately for advanced chips including gate-all-around (GAA) transistors, which the United States has subjected to export controls.<sup>46</sup> The Center is coordinating the work of multiple laboratories, facilitating the establishment of an independent EDA ecosystem by coordinating the participation of companies such as Empyrean, and promoting EDA competitions, such as the Integrated Circuit EDA Elite Challenge, with the goal of “increasing domestic market share of Chinese EDA players.”<sup>47</sup>

Despite the progress, Chinese EDA enterprises have a long way to go, and even Empyrean in a recent prospectus noted that it “could not yet cover the full digital circuit process on its own.”<sup>48</sup> Observers believe that “it is more likely in the medium term that the PRC will make headway in EDA tools for processes at the 14 nm level or above.”<sup>49</sup>

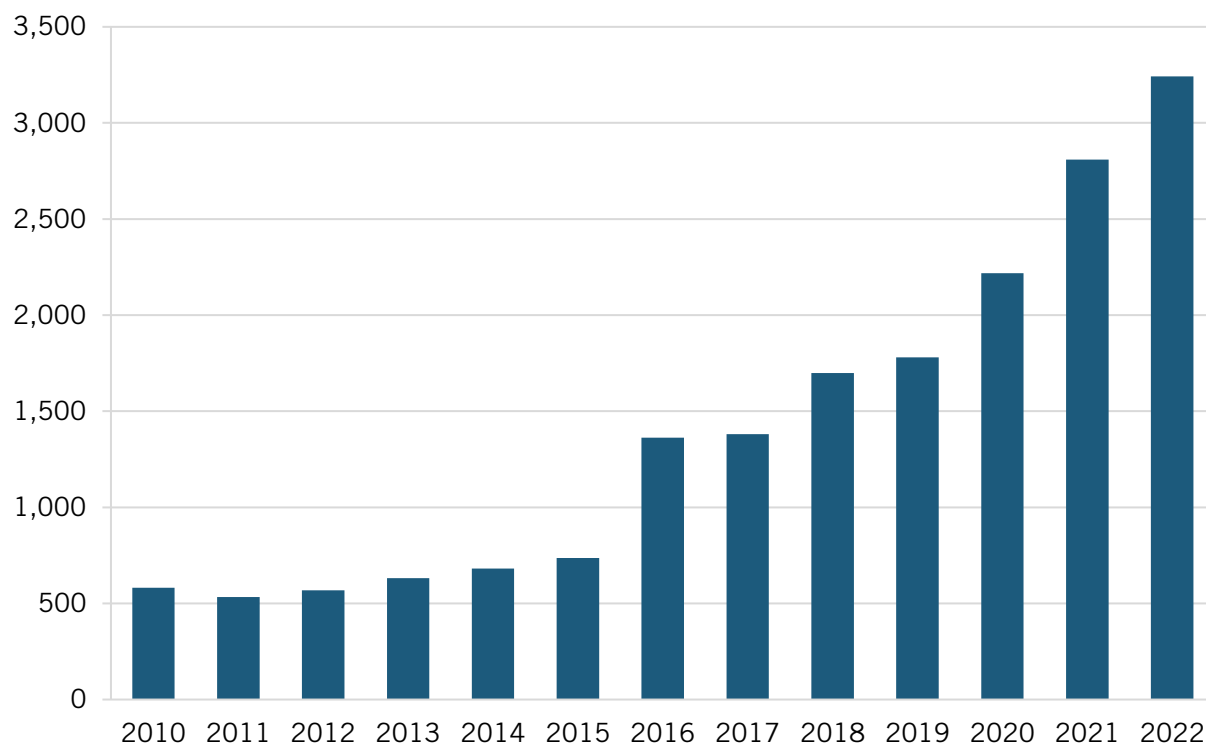
### Design and Fabrication

Semiconductor chip design and semiconductor fabrication are clearly separate steps in the semiconductor production process (and ordinarily would be evaluated separately), but since China's progress in one is so intimately linked to its progress in the other, this section will consider both largely simultaneously, but segment by advanced-node and mature-node logic chips and then memory.

## Semiconductor Design

As one report notes, “China’s design industry has rapidly increased in size since 2015, driven by factors such as the widespread availability of capital (including from both the government and private sector), government support, a desire to localize the industry, demand growth, the acquisition of foreign firms, and downstream users designing their own semiconductors.”<sup>50</sup> In fact, from 2010 to 2022, the number of semiconductor design firms in China increased nearly sixfold, from 582 to 3,243. (See figure 6.) The Chinese IC design industry’s total sales reached 557.4 billion yuan (\$76.7 billion) in 2023.<sup>51</sup> Still, Chinese design firms accounted for only 8 percent of global design revenue in 2022, with no Chinese firms among the top 25 global design firms.<sup>52</sup>

**Figure 6: Number of semiconductor design firms in China<sup>53</sup>**



RISC-V is an open-source instruction set architecture used to develop custom processors for a variety of applications, from embedded designs to supercomputers.<sup>54</sup> RISC-V essentially represents a set of computing standards that provide a common language for designing the processors found in devices such as smartphones, tablets, and Wi-Fi routers.<sup>55</sup>

China is actively embracing and innovating with RISC-V technology.<sup>56</sup> And as industry analyst Paul Triolo wrote, “[E]ager to wean Chinese firms off proprietary Western chip IP and architectures, such as x86 and Arm, Beijing is also fully supporting the development of the RISC-V reduced instruction set architecture. Chinese officials, and industry leaders such as Alibaba and its chip-design arm T-Head, have embraced the RISC-V approach over the past three years.”<sup>57</sup> Indeed, more than 100 “significant” Chinese companies are designing chips with RISC-V today, as are at least 100 more start-ups, according to industry analyst Handel Jones.<sup>58</sup> The top 10 RISC-V star-tups in China have secured funding totaling over \$1 billion.<sup>59</sup> As one

report notes, while many RISC-V “applications are in fairly mundane consumer products,” engineers believe “the technology will eventually take over more demanding tasks.”<sup>60</sup> For example, on November 24, 2023, DAMO Academy (Alibaba’s research division) unveiled three RISC-V-based processors. Elsewhere, “Chinese aerospace scientists have proposed using RISC-V to develop high-performance spaceborne computers,” while Alibaba’s T-Head has designed a RISC-V chip to operate a cloud-styled computing service.<sup>61</sup>

### Advanced-Node Logic Chips

As *The Economist* wrote, “China’s chip industry [remains] far from the technological frontier. Even if Huawei and SMIC eventually succeed at producing 5nm chips, they will remain well behind Samsung, a South Korean tech giant, and TSMC, a Taiwanese foundry, both of which began mass-producing 3nm chips as far back as 2022.”<sup>62</sup> As G. Dan Hutcheson, vice chair of research firm TechInsights, explained, “Ten years ago, [Chinese companies] were two generations behind. Five years ago, they were two generations behind, and now they’re still two generations behind.”<sup>63</sup> However, this doesn’t mean China isn’t making considerable progress in certain pockets.

**In August 2023, Huawei released the Mate 60 Pro smartphone, shown, which deployed the 7 nm Kirin 9000S chip and which was manufactured by SMIC using its SMIC N+2 process technology.**



For instance, in August 2023, Huawei released the Mate 60 Pro smartphone, which deployed the 7 nm Kirin 9000S chip (which Huawei’s HiSilicon design arm architected) and which was manufactured by SMIC using its SMIC N+2 process technology “with capabilities that shocked the world in terms of its performance.”<sup>64</sup> As Dylan Patel of *SemiAnalysis* and Doug O’Loughlin of *Fabricated Knowledge* commented, “Comparing the MatePro’s [chip] to Qualcomm’s chip, made



by Samsung, it is probably only 18 months behind and in some respects it's actually just as good. ... People didn't think China would be capable of producing this."<sup>65</sup>

Patel and O'Loughlin observed that the Mate Pro's network performance (upload/download) speeds were on par with that provided by Qualcomm's chips (and probably superior to those of the latest iPhone) and that its GPU/AI features (supporting phone features such as gaming, videos, and cameras) were mostly on par with those of competitors.<sup>66</sup> They commented that "it's a really good chip, with both hands tied behind their back [meaning it was manufactured without leading-edge EUV lithography equipment, but with older DUV equipment]" and "at worst it's 18 months behind [global leading edge]."<sup>67</sup> As Patel concluded:

Put simply, [the] Kirin 9000S is a better designed chip than the West realizes. It has solid power and performance. Even with the lackluster export controls, this is a leading edge chip that would be near the front of the pack in 2021, yet was done with no access to EUV, no access to cutting edge US IP, and intentionally hampered. We cannot overstate how scary this is.<sup>68</sup>

Because SMIC could not make Huawei's Kirin processor using the latest EUV equipment due to the aforementioned export controls, it used existing DUV equipment it possessed (pre-export controls) to employ a technique called "double patterning"—a process that uses multiple laser passes to etch IC designs at sub-20 nm resolutions—to manufacture at 7 nm.<sup>69</sup> However, this process is time consuming and expensive, making it difficult to manufacture using this process at volume scale, which explains why analysts expected Huawei to ship only 7 million of the MatePro phones in 2023, and perhaps 40 million in 2024.<sup>70</sup> Nevertheless, Patel has contended that SMIC's N+2 process represents a "real, high volume production process technology" with strong yields.<sup>71</sup>

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**The SAQP case reflects that Chinese semiconductor developers are attempting to be as innovative as they can possibly be at process innovation, at least with the assets and technologies available, even if these are clearly behind the global frontier currently.**

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On Friday, March 22, 2024, Huawei (presumably with SMIC) announced that it had filed for patents for a technology called "self-aligned quadruple patterning," or SAQP: a technique for etching lines on silicon wafers multiple times to increase transistor density.<sup>72</sup> The SAQP method described in Huawei's patent application involves etching lines on silicon wafers multiple times to boost transistor density, reduce power consumption, and potentially increase performance.<sup>73</sup> Already, SiCarrier, a Chinese state-backed chipmaking developer, had been issued a SAQP-related patent in late 2023 that illustrates how to employ DUV and SAQP processes to achieve certain technical thresholds evident on 5 nm chips. The SAQP technology represents a similar approach to one Intel explored when looking to move to sub-10 nm processes, but as Anton Shilov wrote, "In the Huawei and SMIC case, quadruple patterning is the only technique that increases transistor density using the tools that the contract chipmaker already has."<sup>74</sup> In other words, the case reflects that Chinese semiconductor developers are attempting to be as innovative as they can possibly be in process innovation, at least with the assets and technologies available, even if these are clearly behind the global frontier currently. It's also evidence that China is trying to reach a state of "good enough" to have sufficient domestic

technologies to produce information communications and telecommunications (ICT) goods servicing domestic markets.

SMIC is currently developing its SMIC N+3 “5 nm” process that should feature 130 million transistors per square millimeter ( $\text{mm}^2$ ). As Patel has written, “If nothing changes with current restrictions, we expect Huawei and SMIC to have a true 5nm-based chip in 2025 or 2026 with large scale AI chips not so long after.”<sup>75</sup> Ultimately, Patel has argued that “SMIC is at most only a few years behind Intel and Samsung” and “at worst only a handful years behind TSMC.”<sup>76</sup> However, as noted, most analysts would peg this gap at closer to five than just a couple years.

Elsewhere, a plethora of Chinese competitors are designing AI chips, including Huawei (with its Ascend chip), Biren, Tencent, Alibaba, Baidu, and MetaX. Patel has asserted that these firms “will soon be able to deliver on chips that are on par with Nvidia’s A100 [using] SMIC 7nm in 2 years at significant volumes.”<sup>77</sup> Commenting on Huawei’s Ascend GPU chips, one observer (requesting anonymity) noted that Huawei’s highest-end product is likely comparable to an NVIDIA H800 in most features save for energy efficiency. Huawei has developed a proprietary software platform, called “CANN,” that helps developers use its chips to build AI models.

In the GPU space, Biren Technology’s BR100 processor competes in the market against NVIDIA’s H100. The H100 features 80 billion transistors on the TSMC N4 process node whereas the BR100 is only 3 billion transistors behind the 7 nm process node.<sup>78</sup> Hong Zhou, cofounder and CTO of Biren Technology, asserts that the BR100’s architecture optimizes the data flow in depth, solving the bottleneck of data migration and insufficient parallelism through six technical characteristics, which means the BR100 chip has “achieved leapfrog progress” in performance and energy efficiency.<sup>79</sup> While the BR100 delivers powerful performance speeds, industry observers note that NVIDIA’s CUDA ecosystem, which tightly integrates hardware and software, is likely more attractive and has made NVIDIA an essential partner for developers and researchers.<sup>80</sup>

Chinese central processing unit (CPU) chipmaker Loongson recently launched its new 3B600 and 3B700 processors, which the company claims match Intel’s 10th-generation chips in single-core performance.<sup>81</sup> The company claims its latest iterations have seen up to a 20-fold gain in single-core capabilities, though company vice president Zhang Ge admits the company’s “chips lag mainstream offerings for multi-core” processors.<sup>82</sup> Nevertheless, China’s government is eyeing the company’s homegrown solutions as attractive offerings for the country’s lucrative education and government markets. Elsewhere, Wuhan Xinxin is building a factory that will be able to produce 3,000 12-inch high-bandwidth memory (HBM) wafers a month to expand China’s domestic base of AI-capable microchips.<sup>83</sup>

### Larger-Node Chips

As noted, larger-node chips, also known as legacy or mature chips, are 28 nm in size or larger.<sup>84</sup> But as one report notes, “Despite the name, legacy chips are not stale technology. The connotations associated with terms like ‘mature,’ ‘older,’ and ‘legacy’ are misleading because these categories of chips are constantly being refined for new requirements and applications.”<sup>85</sup> Rather, larger-node chips are foundational to many markets, including the industrial, aerospace, and defense sectors. Innovations in these chips include the use of wide bandgap materials such as silicone carbide (SiC) and gallium nitrate (GaN), important in clean energy applications.

Elsewhere, as one commentator noted, “The main innovation for the auto industry is happening on mature nodes. They need to be very energy efficient and safe.”<sup>86</sup>

China is likely to become an ever-more significant player in this sector of the market, but its basis for competitive advantage here is likely to be more predicated on massive scale (supported by large, state-driven industrial subsidization) that facilitates price-driven, not innovation-driven, competition.<sup>87</sup>

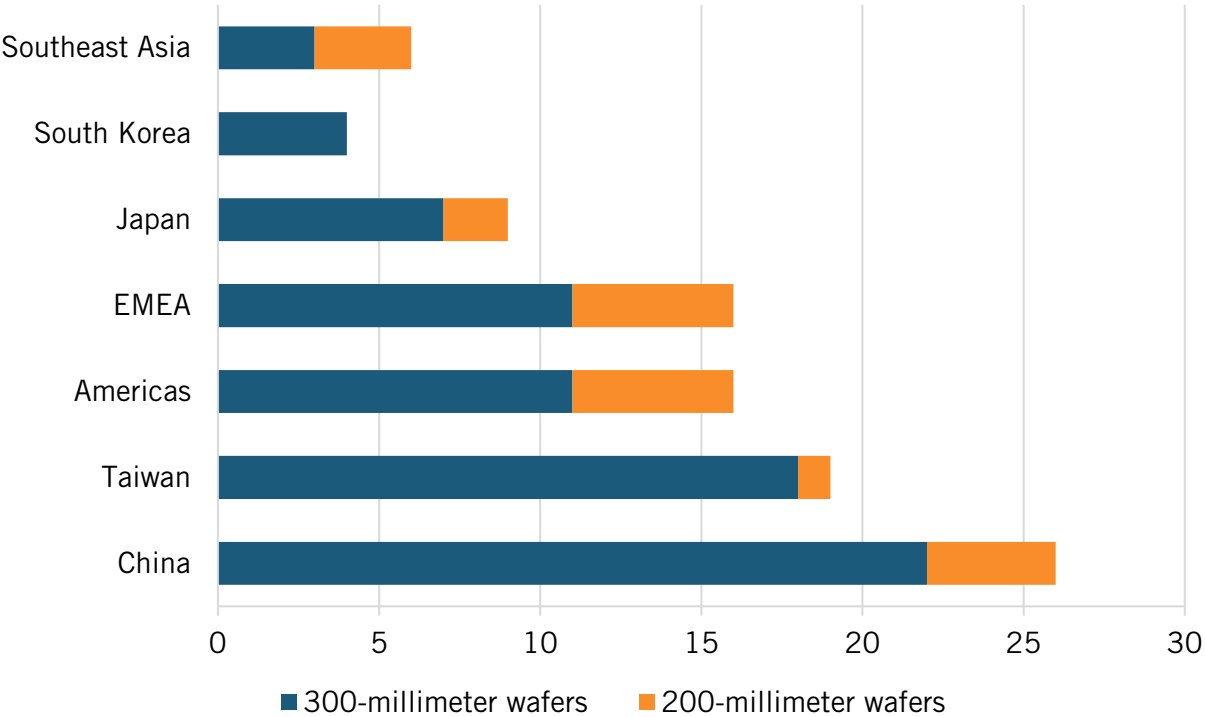
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**China will add more chipmaking capacity than the rest of the world combined in 2024, with 1 million more wafers a month than in 2023.**

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China will account for the most significant share of new semiconductor capacity coming online over the next several years. Indeed, analysts expect that China will add more chipmaking capacity than the rest of the world combined in 2024, with 1 million more wafers a month than in 2023. China’s share of global mature-node production is expected to grow from 31 percent in 2023 to 39 percent in 2027.<sup>88</sup> China currently commands 27 percent of global production capacity for chips in the 20–45 nm range, and 30 percent of global production capacity in the 50–180 nm range. Moreover, analysts expect China to build the most new fabs or major expansions in the 2022 to 2026 time period, with China bringing 26 new facilities online, and Taiwan 19. (See figure 7.) China’s IC output surged 40 percent to 98.1 billion units in the first quarter of 2024, driven primarily by production of legacy chips.<sup>89</sup>

**Figure 7: New fabs and major expansions expected to come online, 2022–2026<sup>90</sup>**



The growth of China’s semiconductor industry—especially in the legacy chip segment—has been driven considerably by massive industrial subsidization designed to help its companies reach

economies of scale in production. Thus, as one report notes, “Chinese firms—supported by lower costs in China due to government subsidies and other factors—are able to offer significantly lower prices.”<sup>91</sup> For instance, China’s microcontroller processor manufacturer Giga Device was offering its products at prices that were 20 to more than 30 percent lower than non-Chinese competitors, such as the French firm ST Microelectronics, across most of 2022 and 2023.<sup>92</sup> Chinese subsidies allow Chinese semiconductor firms to compete in markets without having to earn market-based rates of return, and thus they can sell their products at much lower prices, which places firms that do have to earn market-based rates of return at a significant disadvantage while also disrupting the economics of innovation in the industry, as companies depend on the profits from one generation of semiconductor products to finance the R&D expenses that go into innovating the next generation. This has been a significant challenge for global memory chip manufacturers as well—and this dynamic threatens to extend to every semiconductor subsector which China aggressively subsidizes.

China’s aggressive subsidization of larger-node chips contributes to overcapacity, artificially lowers prices, and disadvantages firms that must earn market-based rates of return, significantly disrupting the economics of innovation in the global semiconductor industry, with deleterious downstream ramifications not just for mature-node chipmakers, but for chipmakers at all node sizes.

### Memory Chips

The semiconductor memory industry has long been a strategic priority for China’s economic development.<sup>93</sup> YMTC, China’s leading NAND maker, is a Chinese state-controlled joint venture launched by the National IC Industry Investment Fund, the erstwhile state university-controlled fabless semiconductor firm Tsinghua Unigroup, and the Hubei Science and Technology Investment Group, supported by \$24 billion in initial government funding allocated for its initial Wuhan factory alone.<sup>94</sup> Launched in 2017, CMXT is another Chinese-government-created semiconductor manufacturer, focusing on DRAM technology. CMXT was established as a project co-led by the local state-owned Hefei Industrial Investment Fund (HIIF) and GigaDevice Semiconductor Beijing (a Chinese designer of flash memory chips), with HIIF kicking in \$8 billion to launch the effort.<sup>95</sup>

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**China’s aggressive subsidization of larger-node chips contributes to overcapacity, artificially lowers prices, and disadvantages firms that must earn market-based rates of return, significantly disrupting the economics of innovation in the semiconductor industry.**

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As Triolo wrote, “Memory is a very different sector than logic, highly commoditized and competitive, with no legacy nodes, requiring companies to constantly upgrade toward the most advanced processes.”<sup>96</sup> As he noted, “YMTC had moved rapidly up the NAND manufacturing curve, producing 128-layer NAND and moving rapidly toward more advanced processes, at 232 layers and above.”<sup>97</sup> A *TechInsights* article finds that YMTC introduced “the first 200+ layer 3D NAND Flash” on the market, ahead of rivals Samsung, SK Hynix, and Micron.<sup>98</sup> YMTC grew incredibly fast after its launch, reaching 5 percent of global market share by 2021, on a track to surpass 10 percent by 2027.<sup>99</sup> In 2022, Apple considered acquiring YMTC’s NAND memory chips for use in iPhones and iPads to be sold in China, and reportedly wanted to increase the order up to 40 percent of the chips required for all iPhones.<sup>100</sup> However, in December 2022, the

U.S. Department of Commerce’s Bureau of Industry Security (BIS) added YMTC to its “Entity List”—a trade “blacklist” that subjects listed entities to export licensing requirements—and this action appears to have significantly stymied the company’s progress. In the months that followed, YMTC laid off 10 percent of its staff and reduced its equipment procurement and expansion plans.<sup>101</sup>

For its part, CXMT has become the workhorse DRAM company in China, but “the company faces significant challenges in the execution of its roadmap” and analysts don’t regard it as competing at the leading edge.<sup>102</sup> The company is “racing to produce China’s first domestic high bandwidth memory, a critical component in artificial intelligence computing.”<sup>103</sup> However, as Counterpoint research semiconductor analyst Dan Wang noted, “When your DRAM technology already lags behind global rivals, that puts your HBM technology at a disadvantage to be competitive in a fully commercial market ... It’s not that easy for China’s national champion to break the international dominance in the sector. Its primary objective remains fulfilling China’s domestic demand.”<sup>104</sup>

Ultimately, as Triolo noted, while “key players like memory makers YMTC and CXMT [are] hobbled by lack of access to cutting-edge tools, [they are] still capable of producing useable memory”<sup>105</sup> In fact, TrendForce projects that the Chinese industry’s share of the NAND flash market will have peaked at 31 percent in 2023 before declining to 18 percent by 2024, while its share of the global DRAM market will fall from 15 percent in 2022 to 12 percent by 2025.<sup>106</sup> Nori Chiou, an investment director at White Oak Capital and a former analyst who looked at the IT sector, estimates that Chinese chipmakers lag their global rivals by a decade in HBM.<sup>107</sup> To be sure, YMTC (like CXMT) has shown itself to be an innovative firm; however, its lack of access to advanced tooling raises questions as to whether it can maintain its innovative wherewithal.

## Semiconductor Manufacturing Equipment

Lithography represents a crucial step in the chipmaking process, wherein a chip wafer gets inserted into a lithography machine and exposed to DUV or EUV light and a pattern is printed onto a chip’s resist layer through a photomask. In the subsequent process step, “etching,” the wafer is baked and developed, and some of the resist layer is washed away to reveal a 3D pattern of open channels.<sup>108</sup> The essential point is that the lithography process determines just how small the transistors on a chip can be, which is why lithography innovations have been every bit as foundational as those in design to moving the industry toward lower process nodes.

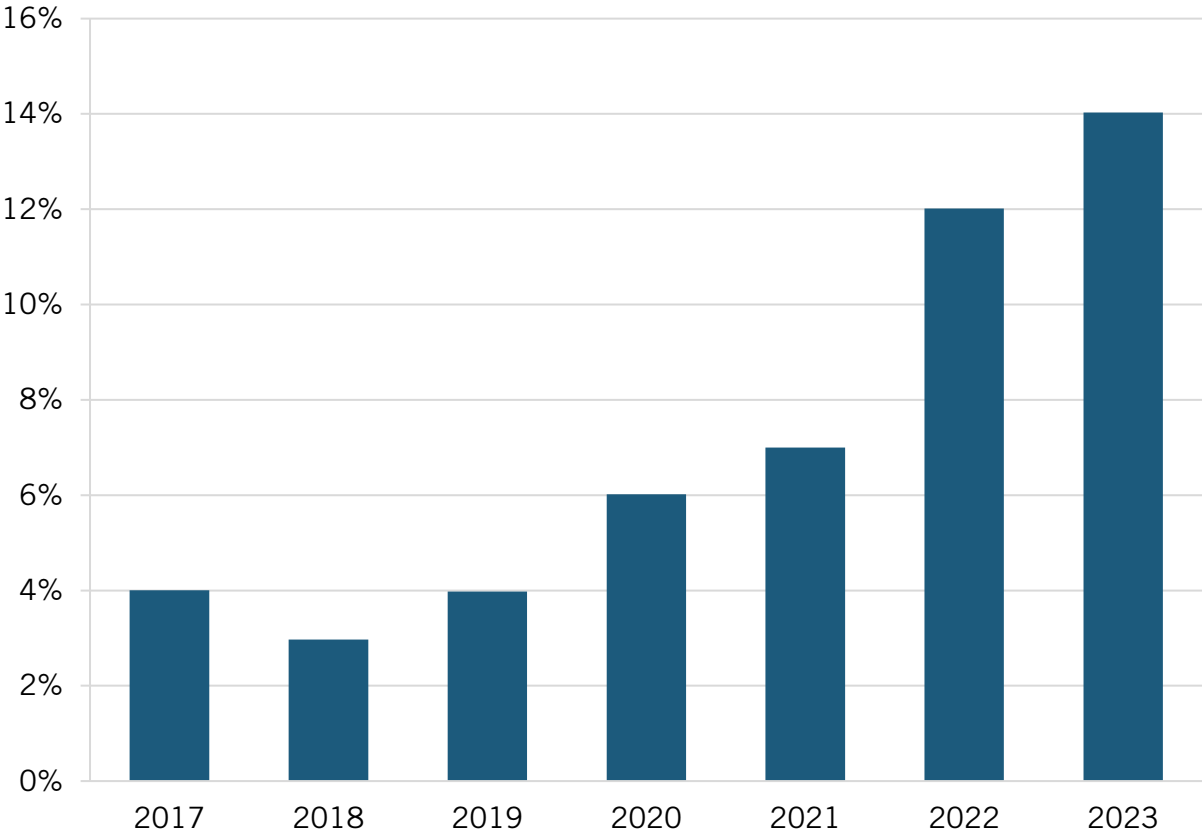
As such, catching up in lithography has long been a central focus of China’s chip aspirations, with the efforts going back to China’s “Project 02” launched in 2008, called “The Project of Manufacturing Complete Sets of Technology for Very Large Integrated Circuits.”<sup>109</sup> Despite these efforts, as Triolo wrote, “Chinese lithography companies appear to be years behind industry leader ASML and Japanese firms such as Nikon and Canon, making lithography one of the critical technology bottlenecks.”<sup>110</sup> For this reason, Triolo observed that “officials in Beijing are developing new approaches to public-private collaboration to push innovation on key technologies, such as advanced lithography ... easing the transfer of advanced state-backed R&D to designated private sector companies, by pushing companies to work together on critical technologies, and by pursuing approaches that have been successful in other sectors.”<sup>111</sup>

Shanghai Micro Electronics Equipment (SMEE) Group, China’s leading lithography developer, claimed in December 2023 to have successfully developed a 28 nm lithography machine, the

SSA/800-10W.<sup>112</sup> (By comparison, TSMC was manufacturing 28 nm chips by 2011, and Intel was by at least 2014. Intel and TSMC are now, or imminently about to be, delivering chips at the 2 nm or even sub-2 nm level.)<sup>113</sup> Nevertheless, as one observer commented, “This advancement represents a major leap in China’s quest to close the technological gap in the global chip industry.”<sup>114</sup> Despite the claimed breakthrough, analysts have raised questions about how long it will take SMEE to produce such machines in bulk. SMEE’s current SSA600 series can utilize 90 nm, 110 nm, and 280 nm processes.

Naura Technology Group represents China’s largest chip production equipment manufacturer, its tooling focused on the etching process, followed by AMEC, which makes deposition equipment. Overall, the domestic market share of Chinese producers of wafer-fabrication tools rose from 4 percent in 2019 to an estimated 14 percent in 2023. (See figure 8.)

**Figure 8: Chinese chip-fabrication equipment makers’ share of Chinese market<sup>115</sup>**

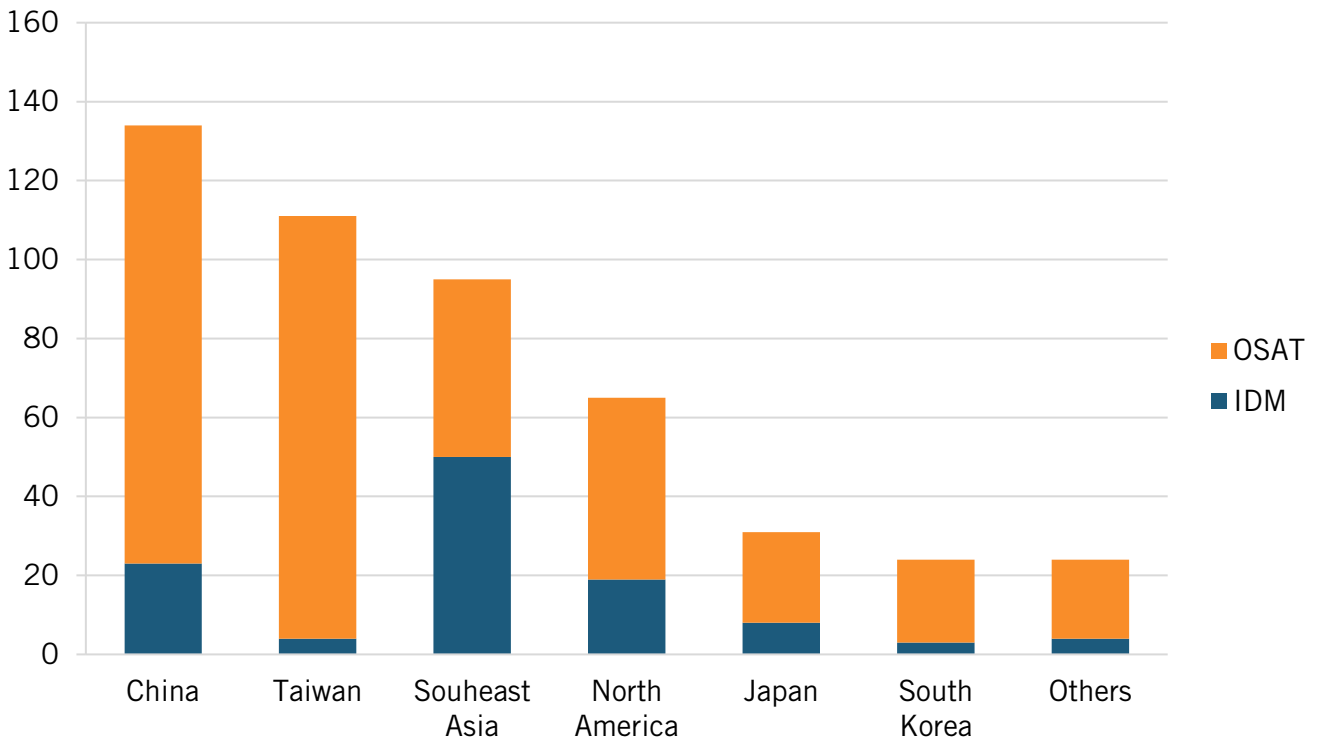


**Semiconductor Assembly, Test, and Packaging**

After front-end chip fabrication, wafers are typically sent to other facilities for back-end manufacturing activities such as ATP. At this step, chips are cut from the silicon wafer, tested for performance, and packaged to protect the chip and allow for its integration into finished electronic devices by attaching electrical interconnections.<sup>116</sup> Semiconductor ATP generally occurs through one of two business models: 1) as in-house ATP services performed by IDMs and foundries after fabrication, or 2) by outsourced assembly and test (OSAT) firms, which perform ATP activities for third-party customers.<sup>117</sup>



**Figure 9: Number of ATP facilities per country/region, 2021**<sup>118</sup>



As of 2021, China accounted for 27 percent (134) of the world’s 484 ATP facilities. (See figure 9.) By August 2023, Chinese ATP firms commanded 38 percent of the market, with the five largest OSAT players—JCET, HT-Tch, TF, LCSP, and Chippacking—all being Chinese.<sup>119</sup>

ATP has historically been viewed as labor intensive and lower value added than design and fabrication, explaining why, historically, firms have set up ATP facilities to a larger extent in developing countries.<sup>120</sup> However, one observer noted that “packaging is the new pillar of innovation in the semiconductor industry—it will change the industry dramatically” as new technologies enable chips to be combined and stacked, and their performance enhanced.<sup>121</sup> Packaging matters for the kind of high-power semiconductors needed for AI applications. Indeed, a shortage of a particular type of packaging known as Chip on Wafer on Substrate, or CoWoS, has been a key bottleneck in the production of Nvidia Corp’s AI chips.<sup>122</sup>

Indeed, as Triolo noted, “Packaging is now becoming a key part of overall production, with back-end packaging being designed into the entire production process, from EDA tools, to integration of IP from companies such as ARM, to 3-d packaging designs that enable greater functionality with a mix of semi-conductors at different levels of complexity.”<sup>123</sup> He further observed, “When it comes to design and packaging, some Chinese firms are using chiplet design already, which is a design approach that integrates chips produced using different processes on one substrate, along with advanced packaging technologies, including 2.5- and 3-d packaging.”<sup>124</sup>

However, industry observers at an ITIF roundtable contended that “while China has made some progress, it really doesn’t have that capability on the leading edge” of semiconductor ATP.<sup>125</sup>

## Advanced Semiconductor Research

Chinese researchers stand at the forefront of innovating some semiconductor technologies, although the question is the extent to which these can be commercialized and scaled by industry. For instance, Professor Liu Kaihui of Peking University has developed new wafers for semiconductors that are just one atom thick (thereby termed “2D”). These new 30.5 centimeter wafers are thinner and more efficient and could “cheaply and potentially revolutionize the semiconductor industry,” its creators claim.<sup>126</sup> Elsewhere, researchers from China and the United States have jointly created a new type of stable semiconductor graphene, which exhibits performance 10 times higher than silicon and 20 times larger than the performance of the other two-dimensional semiconductors.<sup>127</sup>

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### China’s semiconductor industry is not nearly as R&D-intensive as other leading nations’ semiconductor sectors.

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In March 2024, at the IEEE International Solid-State Circuits Conference in San Francisco, Professor Zhou Jun and his team from the University of Electronic Science and Technology of China (UESTC) unveiled what’s been billed as “the world’s most energy-efficient AI chips for mobile devices.”<sup>128</sup> The chips’ design leverages a novel architecture that addresses AI chips’ thirst for power through multiple optimizations, including dynamic computation engines, an adaptive noise suppression circuit, and an integrated keyword and speaker recognition circuit. A UESTC press release noted, “The chip achieves a recognition energy consumption of less than two microjoules per instance, with an accuracy rate exceeding 95 per cent in quiet scenes and 90 per cent in noisy environments, setting new global benchmarks for both energy efficiency and accuracy.”<sup>129</sup> At the conference, the UESTC team also unveiled a chip that helps detect seizures in individuals with epilepsy that achieved a detection accuracy rate of over 98 percent.<sup>130</sup>

In July 2024, Chinese scientists announced development of what could be the fastest analogue-to-digital converter (ADC) for military use. The device can reduce the time delay of electronic warfare receivers from nanoseconds to picoseconds, or one-trillionth of a second. The chip technology would make radar signal detection and responses 91.5 percent faster than currently deployed technologies, nearly doubling the speed of combat to potentially give the Chinese military a critical edge. The chip is based on mature 28 nm process technology.<sup>131</sup>

## ANALYSIS OF INNOVATION INPUTS TO CHINA’S SEMICONDUCTOR SECTOR

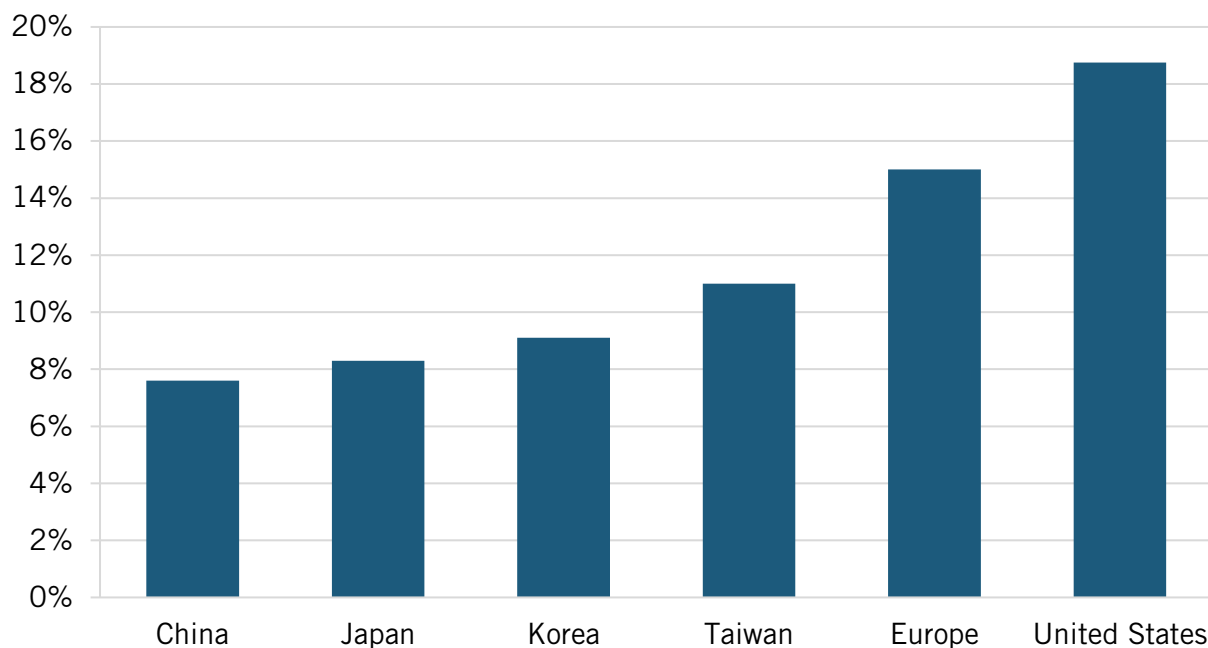
This section examines indicators assessing China’s semiconductor competitiveness at the industry level, considering such factors as R&D intensity, scientific publications, and patenting levels.

### R&D Intensity

Broadly, China’s semiconductor industry is not nearly as R&D-intensive as other leading nations’ semiconductor sectors. In fact, in 2022, China’s semiconductor-sector R&D intensity of 7.6 percent was just 40 percent of America’s 18.8 percent, and well below the EU-country average of 15 percent, or Taiwan’s 11 percent (South Korea and Japan recorded R&D intensities of 9.1 and 8.3 percent, respectively).<sup>132</sup> (See figure 10.) China’s 7.6 percent semiconductor-sector

R&D intensity in 2022 was actually down slightly from the 8.4 percent it had invested in 2018.<sup>133</sup>

**Figure 10: Select nations' semiconductor industry R&D expenditures as a percentage of sales, 2022<sup>134</sup>**



In terms of semiconductor companies' levels of R&D investments (as reported in the "2023 EU Industrial R&D Investment Scoreboard"), Huawei led all Chinese semiconductor-related firms with an R&D intensity of 25.2 percent (although it produces a range of products beyond semiconductors), which was competitive though still below that of global leaders Intel, AMD, and MediaTek, which recorded R&D intensities of 29.6 percent, 25.9 percent, and 25.7 percent, respectively. (See Table 1.) Huawei was the only Chinese semiconductor company in the top 13 on this measure in the study.

**Table 1: Leading semiconductor investors on the "2023 EU Industrial R&D Investment Scoreboard"<sup>135</sup>**

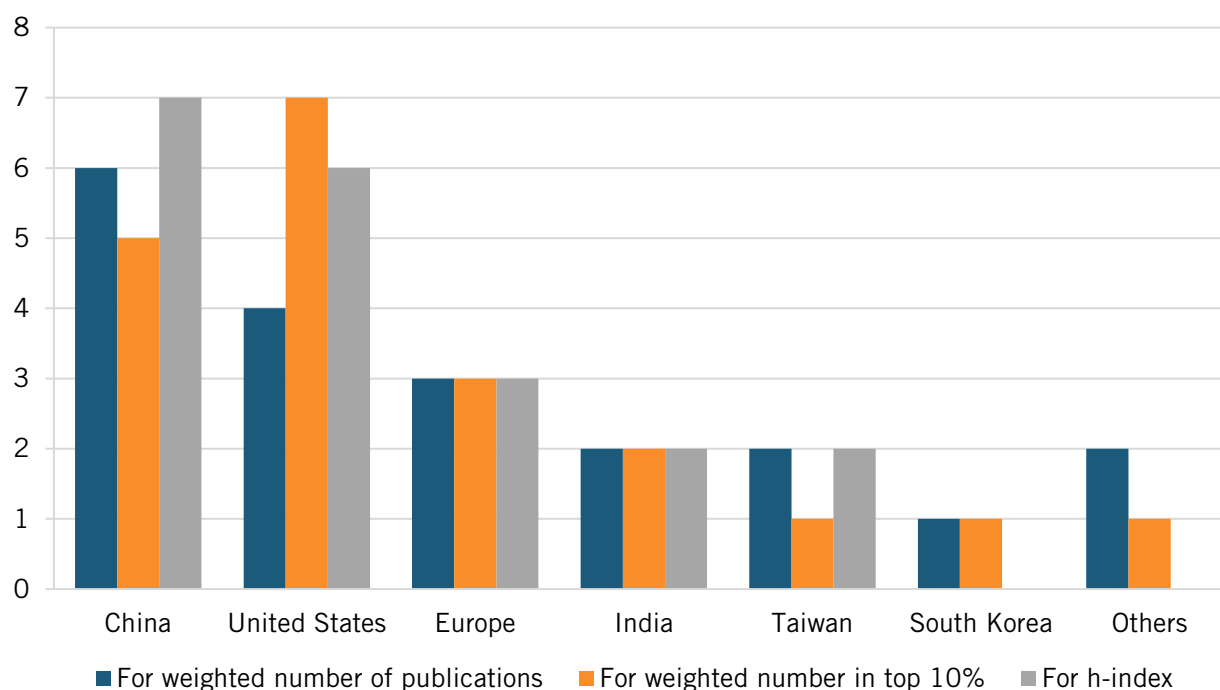
Company	Headquarters	R&D Investment (Billions)	R&D Intensity
Intel	United States	€14.77	29.6%
AMD	United States	€5.40	25.9%
MediaTek	Taiwan	€3.34	25.7%
Huawei	China	€20.70	25.2%
Qualcomm	United States	€7.97	23.9%
Micron Technology	United States	€2.86	19.2%
NXP Semiconductors	The Netherlands	€2.17	17.8%
Broadcom	United States	€5.86	16.4%

Company	Headquarters	R&D Investment (Billions)	R&D Intensity
ASML	The Netherlands	€4.04	14.4%
NVIDIA	United States	€7.99	14.2%
Analog Devices	United States	€1.51	14.2%

## Scientific Publications

As noted, according to ASPI, “The US excels in the design and development of the most advanced semiconductor chips and has a research lead in the technology areas of ... *advanced integrated circuit design and fabrication*.”<sup>136</sup> Nevertheless, Chinese researchers have made impressive strides in the number and quality of their scientific publications in semiconductors. Considering the h-index, a measure of the productivity and citation impact of scientific publications, China counts 7 institutions in the global top 20 research institutions releasing advanced IC and design publications. Specifically, the Chinese Academy of Sciences ranks 6th overall, followed by Peking University (8th), Huazhong University of Science & Technology (9th), Zhajang University (11th), Shanghai Jiaotong University (12th), Hong Kong University of Science and Technology (16th), and East China Normal University (20th).<sup>137</sup> China also places 6 academic institutions in the top 20 for the weighted number of publications on advanced IC design and fabrication, and 5 institutions for the weighted number of publications within the top 10 percent of highly cited publications. (See figure 11.)

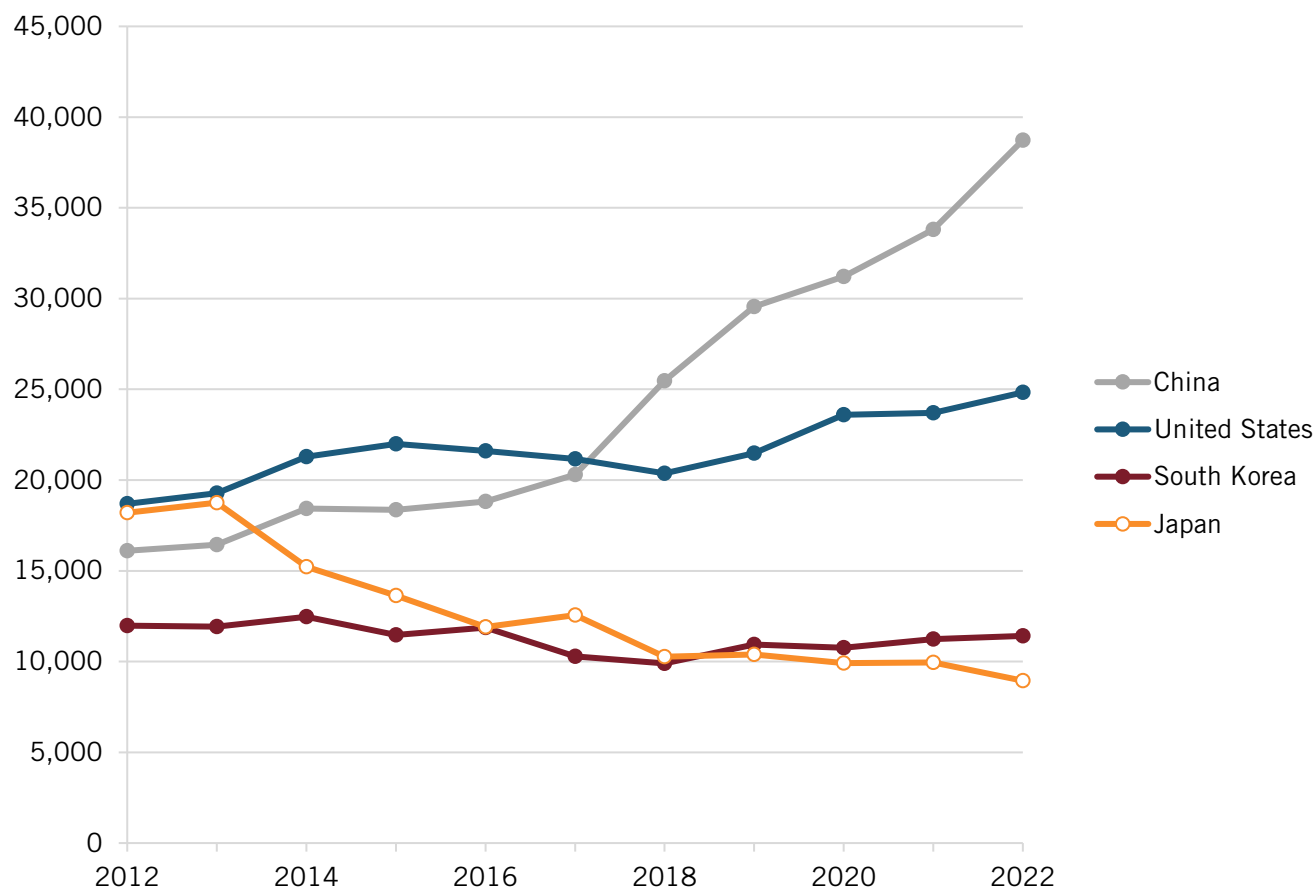
**Figure 11: Number of research institutions ranking among the top 20 institutions globally for their volume and quality of publications in advanced IC design and fabrication<sup>138</sup>**



## Patents

Scientific publications represent knowledge feedstock that can be turned into patent applications, and China has made tremendous strides in increasing its levels of semiconductor patent applications and semiconductor patents granted. In fact, in 2021–2022, 55 percent of global semiconductor patent applications (37,865) were Chinese in origin, more than doubling the 18,223 applications submitted by U.S.-based filers.<sup>139</sup> (See figure 14.) Chinese filers' semiconductor patent applications doubled in number from 2012 to 2022.

**Figure 12: Semiconductor patent applications, by country of origin<sup>140</sup>**

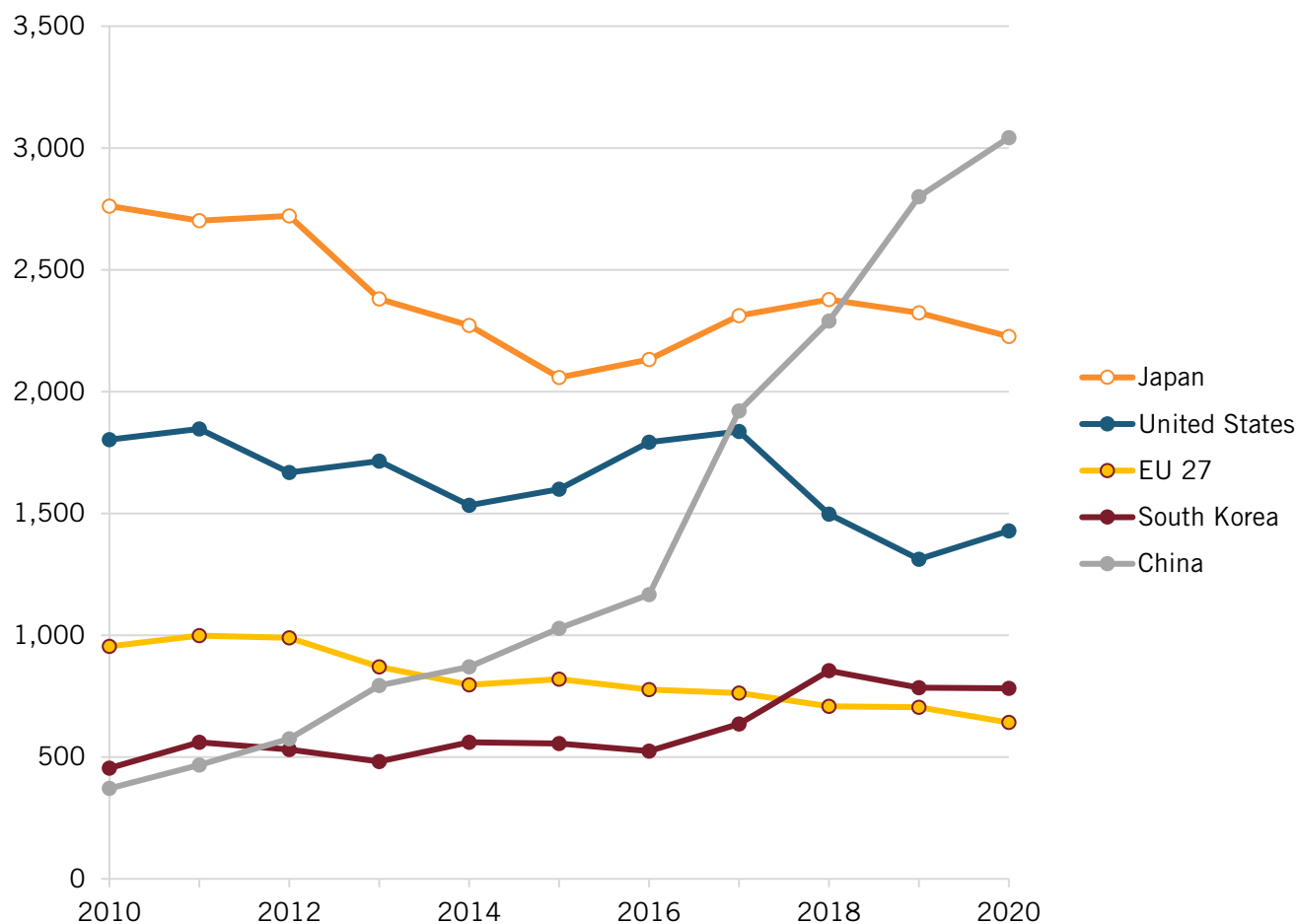


While China has long been known for filing significant numbers of dubious patent applications—in part through incentives that compensate filers for submitting regardless of the applications' merits—the evidence shows that it's not simply fluff: Chinese entities are indeed receiving semiconductor patents in record numbers. The Patent Cooperation Treaty (PCT) allows for innovators to seek protection for an invention simultaneously in each of a large number of countries by filing an “international” patent application.<sup>141</sup> From 2010 to 2020, Chinese entities increased their levels of PCT semiconductor patents awarded nearly 30-fold (from 122 to 3,474), exceeding Japan's count by 2020, while the number of patents being awarded to U.S. filers fell by 22 percent. (See figure 13.)

It is of note that while Japan is last among the countries examined here for semiconductor applications, they are second in patents granted. This could be attributed to the high quality of patents published in Japan in comparison with other countries. In 2020, Japan obtained 2,679

patent grants out of its 9,963 applications, a 27 percent grant rate. China, on the other hand, had just 11 percent of its patent applications granted. Unlike for China, Japanese filers likely apply for fewer potentially suspect patents, explaining its high ranking among semiconductor patent grant recipients.

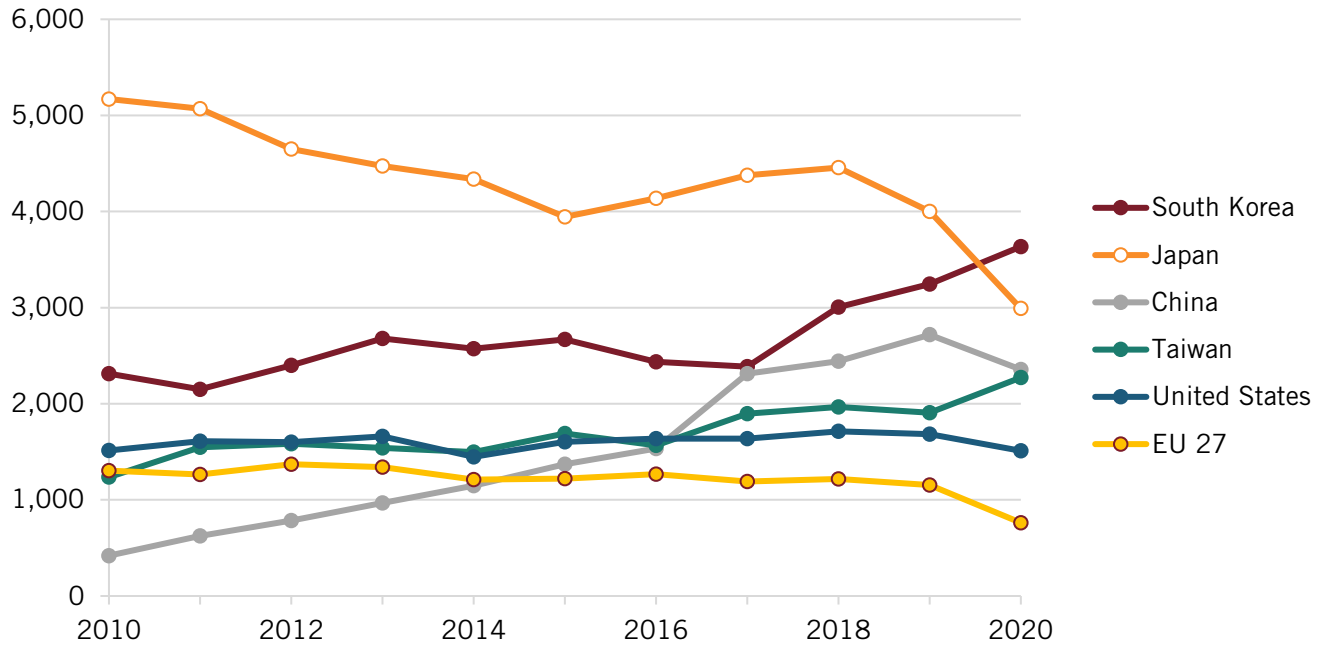
**Figure 13: Number of PCT patent grants in semiconductors<sup>142</sup>**



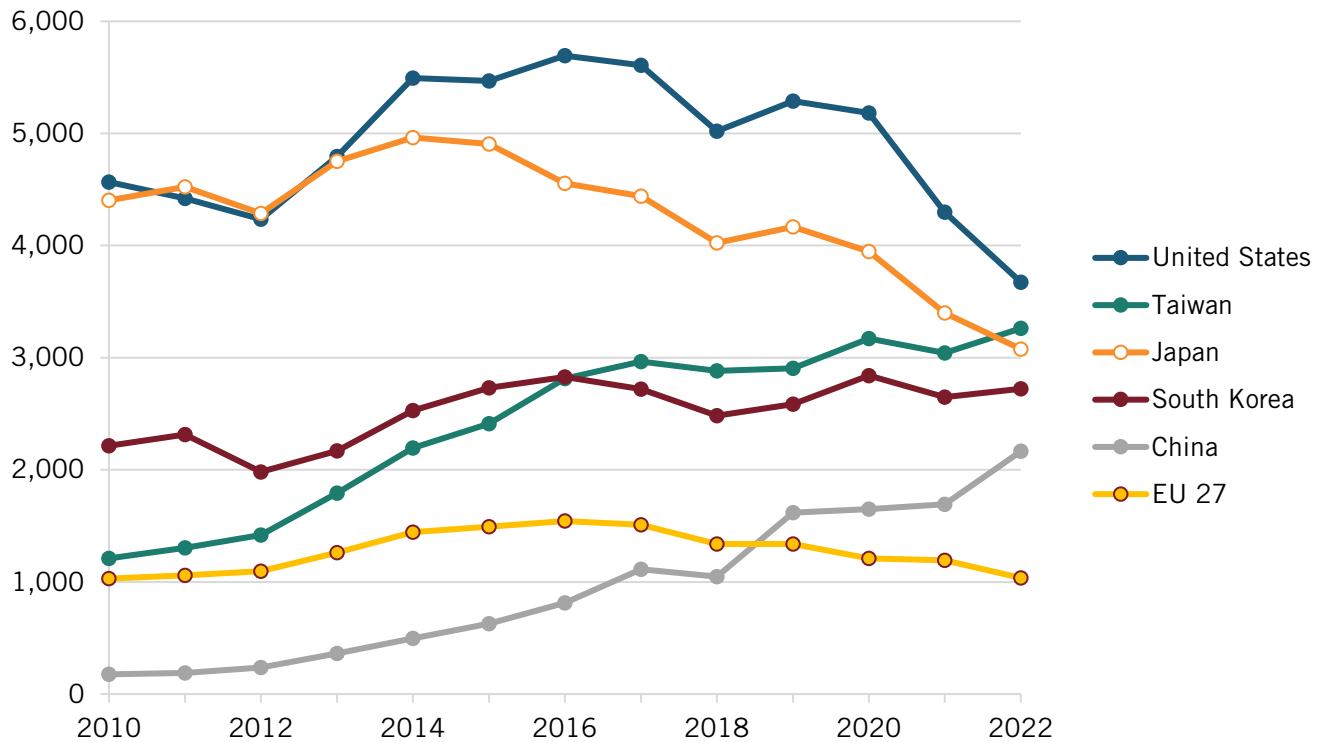
Considering IP5 patent family grants (referring to collective grants from the Chinese, European, Japanese, South Korean, and U.S. patent offices), China clocked in third for semiconductor patent family grants in 2020, with 2,335 issued, behind 3,645 for South Korean filers and 2,990 for Japanese ones.<sup>143</sup> China's number of IP5 patent families in semiconductors increased nearly fivefold during the prior decade, from just 420 in 2010 to 2,335 in 2020, while Japan's number fell by nearly 40 percent. (See figure 14.)



**Figure 14: Number of IP5 patent families in semiconductors<sup>144</sup>**



**Figure 15: USPTO semiconductor utility patents granted<sup>145</sup>**

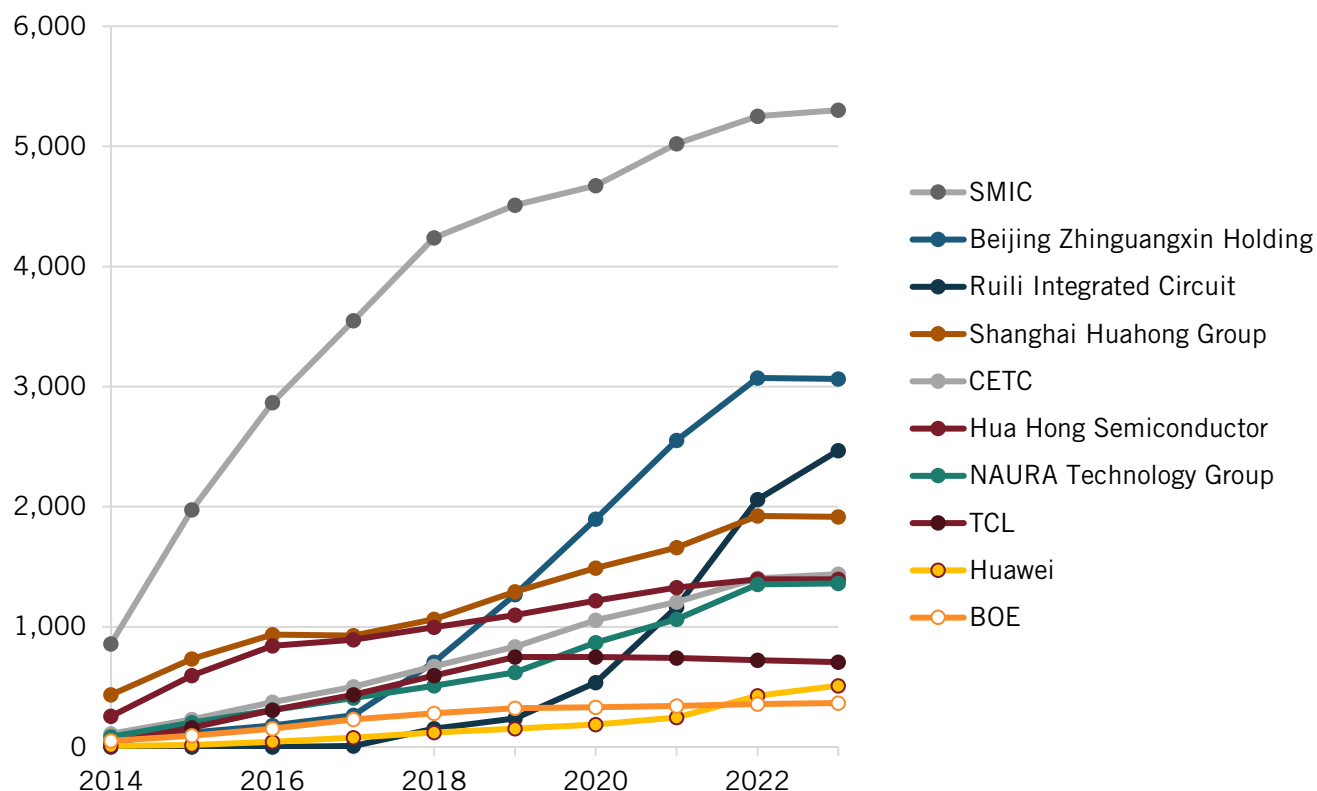


Similar trends are apparent when considering semiconductor utility patents issued by the U.S. Patent and Trademark Office (USPTO). From 2010 to 2022, the number of patents USPTO issued to Chinese filers increased 12-fold, though China still trailed the United States and Japan

(whose semiconductor patent levels were dropping) and Taiwan and South Korea (whose levels were gaining) over this time. (See figure 15.)

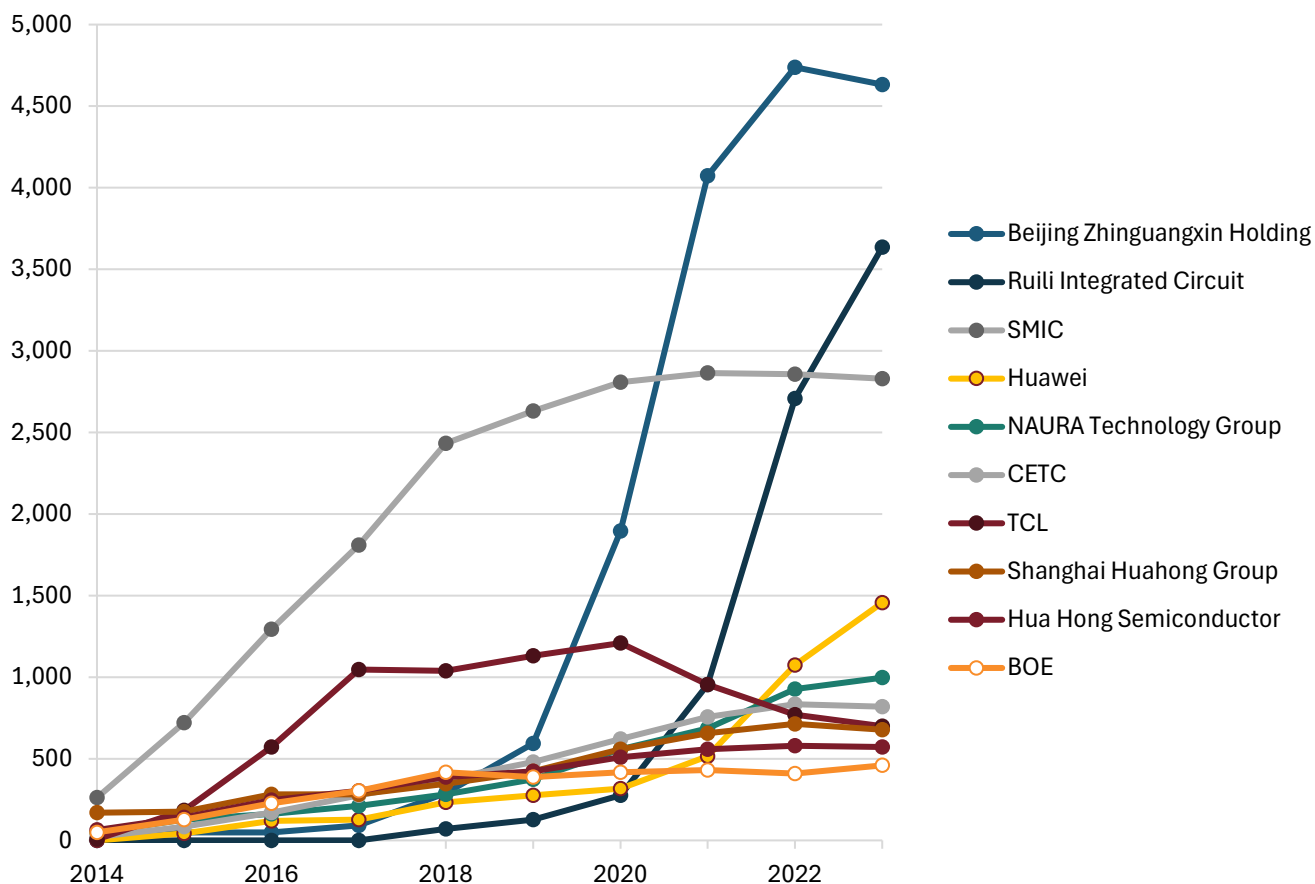
Over the past decade, the overall size of Chinese companies' patent portfolios has correspondingly grown significantly. The size of SMIC's patent portfolio increased from under 1,000 patents in 2014 to over 5,000 by 2023. (See figure 16.) Likewise, the number of patents held by Beijing Zhiguangxin Holding—which has assumed the holdings of the now defunct Tsinghua Unigroup and spawned them into UNISOC, now China's largest mobile phone chip designer—increased from just 7 to 4,632 over this time, while Huawei's number of semiconductor patents increased from virtually nil to nearly 1,500.

**Figure 16: Patent portfolio size of leading Chinese semiconductor companies<sup>146</sup>**



LexisNexis, an American data analytics company, has created a “PatentSight’s Patent Asset Index,” which seeks to evaluate the “competitive impact” of a company’s patent portfolio, doing so by multiplying the “technology relevance” of a patent, which is based on the number of citations a patent receives and its “market coverage,” which is based on the gross national income of the regions where the patent is protected.<sup>147</sup> Examining the “Semiconductor Device Manufacturing” category on the Patent Asset Index shows that Beijing Zhiguangxin Holding holds the strongest patents out of China’s top-10 largest semiconductor patent holders, followed by Ruili Integrated Circuit and SMIC. (See figure 17.) However, despite this growing strength, as one commentator noted, Beijing Zhiguangxin Holding represents only 2 percent of the total Semiconductor Device Manufacturing Index and “while there is a clear upward trend, it is major players like Samsung and TSMC that still hold the ‘lion’s share’ of the global semiconductor technology market.”<sup>148</sup>

**Figure 17: Top 10 Chinese semiconductor companies' scores on the Patent Asset Index<sup>149</sup>**



## COMPANY CASE STUDIES

This section provides case study analyses of several Chinese semiconductor companies; they were selected randomly from Chinese semiconductor companies included in the “2023 EU Industrial R&D Investment Scoreboard” report.

### Biren

Founded in September 2019 and headquartered in Shanghai, Biren Technology focuses on developing original general computing systems, establishing efficient software and hardware platforms, and providing integrated solutions in the field of intelligent computing. The company has attracted more than 4.7 billion RMB in financing (\$6.6 million), breaking the record for China’s domestic chip industry. Its corporate strategy has been to first focus on cloud general intelligent computing to gradually catch up with existing solutions in AI training, reasoning, and graphics rendering.

On August 9, 2022, Biren released the first general-purpose Chinese GPU chip, the BR100, in Shanghai and it set a global computing power record. It became the first GPU chip in China to adopt chiplet technology, the first to adopt a new generation of host interface PCIe 5.0, and the first to support the CXL interconnection protocol. The peak computing power of a single chip reached the PLOPS (peta floating-point operations per second) level, which was then three times faster than flagship products sold by other manufacturers, and marked the first time a Chinese

company had broken the international power record for general-purpose GPUs held by leading global competitors.<sup>150</sup>

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### **Chinese entities are indeed receiving semiconductor patents in record numbers.**

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On September 1, 2022, Biren’s original architecture of “BR100: Large Computing Power Artificial Intelligence General-purpose GPU Chip” won the Super AI Leader Award (thereafter named the SAIL Award) at the 2022 World Artificial Intelligence Conference, standing out from more than 800 entries.<sup>151</sup> (NVIDIA has won previous SAIL awards itself.) Biren has also been recognized by the MLPerf Inference AI benchmark (MLPerf Inference v2.1) led by Turing Award winner David Patterson and jointly established by institutions such as Google, Stanford University, and Harvard University, which is considered one of the most authoritative and influential benchmarks in the world.<sup>152</sup> The benchmark measures how fast systems can process inputs and produce results using a trained model. Biren’s GPU chip BR104 won the world’s first excellent performance in the sellable single card performance in the first two benchmarks of natural language understanding (BERT model) and image classification (ResNet50 model) in the data center inference evaluation.<sup>153</sup> Ultimately, Biren’s innovation potential manifests itself in its world firsts and faster performance compared with global industry leaders such as NVIDIA, as well as the development of more advanced chip products.<sup>154</sup>

According to information provided by Aiqicha, Biren currently possesses 249 patents, 23 software copyrights, and 23 trademarks.<sup>155</sup> USPTO has issued 21 patents to Biren.<sup>156</sup> Biren has been strengthening its R&D partnerships and collaborations with top science and technology universities including Tsinghua University, Fudan University, and Shanghai Jiao Tong University. Regarding organizational structure, Biren’s team is composed of core professionals and R&D personnel in the fields of chip and cloud computing at home and abroad, who possess unique industrial insights and deep technical experience in the fields of GPU, DSA (dedicated accelerator), and computer architecture.<sup>157</sup> Biren has partnered with the Shanghai Advanced Research Institute of Zhejiang University to build the Joint Innovation Center for Scientific Intelligent Computing and Application to collectively promote talent development and technological innovation. The partnership seeks to build related industrial ecology and create a strong innovative computing power foundation for China’s frontier scientific research.<sup>158</sup>

Biren has certainly produced notable innovations and become a credible competitor to NVIDIA and other GPU developers. In October 2023, the United States placed Biren on its Entity List and officially requested that TSMC suspend fabrication of chips for the company. (Biren had tapped TSMC’s 7 nm process node to manufacture its GPU.)<sup>159</sup> Biren has since turned to SMIC, which by 2025 should be producing Biren’s chips in the 5 nm/6 nm/7 nm range.<sup>160</sup> U.S. export controls have certainly slowed Biren, but analysts expect the company to remain a serious competitor in the GPU market going forward.

### **SMIC**

Founded in 2000 and headquartered in Shanghai, SMIC provides semiconductor foundry and technology services to global customers on 0.35 micron (µm) to FinFET process node technologies. (See figure 20.) SMIC has grown to become the largest contract chipmaker in mainland China and the fifth-largest at the global level.<sup>161</sup> Partially owned by the Chinese

government, SMIC is supported by major stakeholders including the National Integrated Circuit Industry Investment Fund and Datang Telecom Group. SMIC offers a broad range of technologies from 0.35  $\mu\text{m}$  to FinFET, with capabilities that include logic, mixed-signal/RF CMOS, high-voltage, SoC, flash, EEPROM, CIS, and LCoS micro-display technology.

SMIC has been capable of producing 7 nm chips since 2021, according to Canadian industry research institute *TechInsights*.<sup>162</sup> Based on its comparison and analysis of SMIC's 7 nm against TSMC's, *TechInsights* assessed that "SMIC reaches 7nm without access to western equipment & technologies" and that "SMIC 7nm is truly 7nm technology."<sup>163</sup> Moreover, *TechInsights* found that SMIC "has used 7nm technology to manufacture the MinerVa Bitcoin Miner application-specific integrated circuit (ASIC)."<sup>164</sup> However, SMIC has difficulty with high-volume manufacturing at sub-28 nm levels.

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**Overall, SMIC's core technologies/products may be considered innovative within the mainland China's context, but at the leading edge, they remain behind global peers.**

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For the development of its 7 nm technology, analysts reported that SMIC achieved an "Intel-like breakthrough" in its leap from 14 nm to 7 nm.<sup>165</sup> Moreover, according to *TechInsights*, SMIC's scaling puts its technology in the same 7 nm group as Intel, TSMC, and Samsung; specifically, SMIC 7 nm shows more relaxed scaling for contacted poly and metal and fin pitches, setting it apart from TSMC N7+ and Intel 10 nm.<sup>166</sup> Overall, SMIC's core technologies/products may be considered innovative within the mainland China's context, but at the leading edge, they remain behind global peers.

**The Semiconductor Manufacturing International Corporation (SMIC), shown here, provides semiconductor foundry and technology services to global customers on 0.35 micron to FinFET process node technologies.**



According to SMIC's Q4 2022 financial statements, the company has invested about \$197.5 million in R&D, accounting for the largest spending activity of the firm. The statement also shows a continued increase in expenses for R&D over \$182.9 million in 3Q 2022 and \$172 million in 4Q 2021.

At the organizational level, SMIC has aimed to “develop everything needed in house” from training local semiconductor engineers to collaborating with universities as well as hiring talent from Taiwan and the United States who helped train local talent.<sup>167</sup>

Apart from in-house training and R&D, SMIC's innovation ecosystem has also been boosted by collaboration and partnerships with both industry and academia. Even as long as a decade ago, SMIC already had several R&D teams collaborating with partners from various organizations on new fabrication technologies dedicated to “mitigate risks as well as develop differentiated processes aimed at specific applications.”<sup>168</sup>

## CHINA'S SEMICONDUCTOR STRATEGY

As early as 2014, Xi Jinping noted that “semiconductors [are] a core technology that China should produce domestically.”<sup>169</sup> Indeed, semiconductors represent a central part of Jinping's “pledge to mobilize all means at [China's] disposal to wrest technological supremacy from the United States and other nations.”<sup>170</sup>

The National IC Plan became the centerpiece of China's semiconductor strategy in 2014, with China's State Council setting a goal of China becoming a global leader in all segments of the semiconductor industry by 2030.<sup>171</sup> The IC Plan called for at least \$150 billion in government subsidies—from central, provincial, and municipal Chinese governments as well as a variety of state-owned enterprises (SOEs) from the technology sector and beyond—to enable China to become self-sufficient in every facet of the industry.<sup>172</sup> The “Big Fund” was established to function as a national investment fund, offering targeted support for the development of the semiconductor industry.<sup>173</sup> The 2014 fund raised 138.7 billion RMB (\$23 billion) and the 2019 fund raised 200 billion RMB (\$29 billion). In September 2023, *Reuters* reported that China was planning to start its most extensive state fund to date, with a target of 300 billion RMB (\$41 billion).<sup>174</sup> America's Department of Commerce estimates that China indeed reached its goal of pumping \$150 billion into the sector over the past decade.<sup>175</sup> Renowned semiconductor industry analyst Chris Miller suggested on a recent podcast that, if one adds up semiconductor-sector investments made by Chinese governments at all levels (national, provincial, city), China “has probably invested the equivalent of [America's] CHIPS Act virtually every year since 2014.”<sup>176</sup>

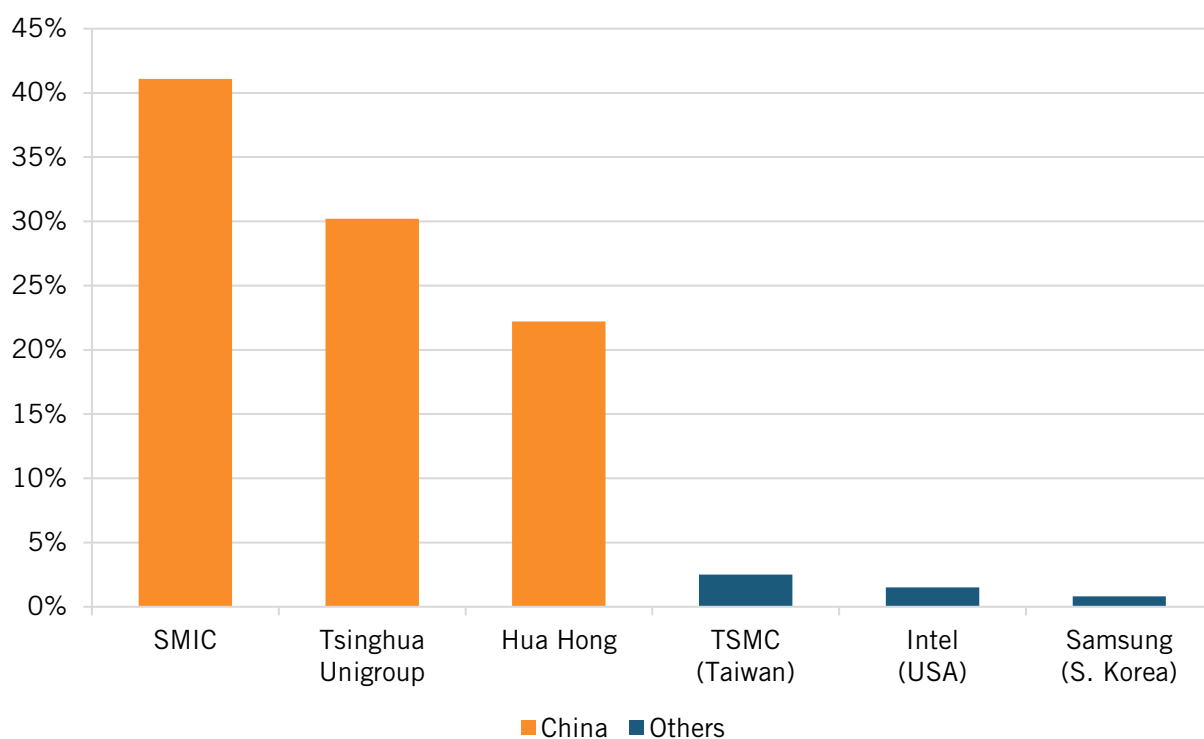
Massive industrial subsidization of Chinese semiconductor firms lies at the core of China's project. As the Organization for Economic Cooperation and Development (OECD) has found, “Government support through below-market equity appears to be particularly large in the semiconductor industry and concentrated in one jurisdiction.”<sup>177</sup> The OECD's study of 21 international semiconductor firms over the years 2014 to 2018 found that government support provided through “below-market equity” (i.e., effectively a subsidy) “amounted to \$5-15 billion for just six semiconductor firms, four of which are from China” (these being Hua Hong, JCET, SMIC, and Tsinghua Unigroup). The report continued to note that, for SMIC and Tsinghua Unigroup, “total government support exceeded 30 percent of their annual consolidated revenue.”<sup>178</sup> In total, the OECD found that Chinese firms together received 86 percent of below-



market equity injections identified in its study and concluded, “For the four Chinese firms covered by this study, government funds have committed equity funding of about \$22 billion in total to date, with the largest share benefitting SMIC and Tsinghua Unigroup, and their subsidiaries. These four firms received about \$10 billion of the initial \$23 billion tranche of National IC Plan funding.”<sup>179</sup> With regard to SMIC, the OECD report found that state subsidies accounted for slightly over 40 percent of the company’s revenues from 2014 to 2018.<sup>180</sup> Of particular import, the OECD study found that there “notably appears to be a direct connection between equity injections by China’s government funds and the construction of new semiconductor fabs in the country.”<sup>181</sup>

When it comes to state subsidies at the firm level—that is, as a percentage of revenue for semiconductor manufacturers (from 2014 to 2018)—Chinese enterprises clearly led their foreign competitors by an order of magnitude. State subsidies accounted for slightly over 40 percent of SMIC’s revenues over this period: 30 percent for Tsinghua Unigroup, and 22 percent for Hua Hong. (See figure 18.) In contrast, this figure was minimal for TSMC, Intel, and Samsung, each, for which revenues identifiable as state subsidies accounted for, at most, 3 percent or less of their revenues over this period.

**Figure 18: State subsidies as a percentage of revenue for chip fabs, 2014–2018**<sup>182</sup>



Chinese semiconductor companies benefit from lower taxes, such as corporate income tax exemptions or reductions, tax credits for R&D, and tax or tariff reductions/exemptions for domestically produced or imported equipment and materials.<sup>183</sup> Subnational governments in China also sell land to Chinese semiconductor companies at reduced prices.<sup>184</sup>

As noted, in the seminal Made in China 2025 strategy, China set a goal of achieving 40 percent self-sufficiency in semiconductors by 2020 and 70 percent by 2025.<sup>185</sup> China’s “Information



Innovation” project, locally called *Xinchuang*, aims to replace foreign with domestic suppliers of critical ICTs, including semiconductor technology.<sup>186</sup> As one report notes, China is seeking “to establish production processes largely free of Western equipment ... This will be a multistage, multiyear process, starting with 40 nanometers and proceeding quickly, likely [in 2024], to 28 nanometers, and then 14, 12/10, and eventually 7 nanometers.”<sup>187</sup>

On April 12, 2024, *The Wall Street Journal* reported, “China’s push to replace foreign technology is now focused on cutting American chip makers out of the country’s telecommunications systems.”<sup>188</sup> The move would impact a variety of U.S. semiconductor companies, including AMD and Intel. The article notes that “[Chinese] officials earlier this year directed the nation’s largest telecom carriers to phase out foreign processors that are core to their networks by 2027.”<sup>189</sup> The effort is similar to one articulated in Document 79, which requires SOEs in finance, energy, and other sectors to replace foreign software in their IT systems by 2027.<sup>190</sup>

Elsewhere, the Chinese government has asked electric-vehicle makers, from BYD Co. to Geely Automobile Holdings Ltd., to sharply increase their purchases from local auto chipmakers as part of a campaign to reduce reliance on Western imports and boost China’s domestic semiconductor industry.<sup>191</sup> China’s Ministry of Industry and Information Technology (MITI) is directly instructing Chinese automakers to avoid foreign semiconductors if at all possible.<sup>192</sup> Such measures leave no doubt that import substitution and achieving self-sufficiency represents an essential goal of China’s semiconductor strategy.

Despite China’s massive investments in the sector, by early 2023, a sense had emerged in Beijing that they weren’t paying the expected dividends. Beijing felt that the “scientist-led approach has not delivered much progress,” nor has “the attempt to use market forces.”<sup>193</sup> Thus, in the wake of the March 2023 National People’s Congress, Beijing revamped oversight of its semiconductor industry by establishing a “leading small group” under vice premier Ding Xuexiang that will have oversight over all aspects of China’s new semiconductor strategy.<sup>194</sup> Triolo speculated that “a high-level decision may have been made that China needs a national-level SOE conglomerate to run overall semiconductor industry policy, in a manner similar to sectors like nuclear with CNNC and aerospace with CASIC,” though this has not yet come to pass.<sup>195</sup> Two aspects that do seem certain to change, however, are that China will increase its investments in basic R&D in semiconductor physics as well as critical technologies such as light sources for advanced lithography. That stems from a recognition that the Big Fund was “initially focused on manufacturing and design, and not enough attention paid to SME.”<sup>196</sup> Certainly, China is revamping its tactics to develop its semiconductor sector, but its hunger to dominate the sector remains unabated.

## WHAT SHOULD AMERICA DO?

The United States made a major commitment to revitalizing the competitiveness of its semiconductor industry when Congress passed the 2022 CHIPS and Science Act. As of August 2024, the CHIPS program office has announced several notable investments from the fund, including \$8.5 billion to support Intel’s construction of semiconductor facilities in four states, \$6.6 billion to support three TSMC funds in Arizona, \$6.5 billion for Samsung, and \$1.5 billion to expand Global Foundries’ operations in New York, among others.<sup>197</sup> Since 2020, semiconductor companies have committed over \$300 billion to building and operating fabs in the United States.

On the research side, the CHIPS legislation features a massive \$5 billion investment in America’s semiconductor industrial commons through the National Semiconductor Technology Consortium (NSTC), whose mission is “to ensure the U.S. leads the way in the next generation of semiconductor technologies by supporting the design, prototyping, and piloting of the latest semiconductor technologies; leveraging shared facilities and expertise to ensure innovators have access to critical capabilities; and building and sustaining a skilled and diverse semiconductor workforce.”<sup>198</sup> Further, the U.S. Department of Commerce has announced a \$200 million investment in a CHIPS Manufacturing USA Institute to create a first-of-its-kind semiconductor manufacturing digital twin institute, which will allow innovators to replicate and experiment with physical manufacturing processes at low cost.<sup>199</sup> These represent critical initiatives whose success will depend on effective implementation and execution going forward. Also, with regard to America’s Manufacturing USA network—of which the CHIPS Institute will become the 18th institute—**Congress should eliminate the automatic five- to seven-year federal funding sunset for the institutes and replace it with a five-year, metrics-based review program with minimum standards of performance focused on the advancement of technology and manufacturing readiness.**<sup>200</sup>

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**The success of the multiple initiatives contemplated under the CHIPs and Science Act will depend on effective implementation and execution going forward.**

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The 25 percent ITC component of the CHIPS Act was one of its most powerful elements—companies could not be certain that they would win a loan or grant, but they were certain that they could take the 25 percent ITC if they put shovels in the ground. As such, **Congress should extend the ITC through the end of this decade** (it currently applies to planned construction beginning before January 1, 2027). **Congress should also extend the 25 percent ITC to firms designing semiconductor chips, not just to those building semiconductor fabs.**

The rapid expansion of China’s semiconductor industry—as has been the case for many other Chinese industries, from electric vehicles and solar panels to high-speed rail—has been powered in part through massive industrial subsidization of a nature and on a scale that clearly violates the country’s World Trade Organization (WTO) commitments. To address this, **the United States should work with like-minded nations to update WTO rules to impose much stiffer conditions on, and penalties for, aggressive industrial subsidization.**<sup>201</sup> **This should start with clarifying the definition of a “public body,” extending it to include state-influenced activities of entities such as state-owned enterprises or private firms.**<sup>202</sup> Further reforms should **insist on timely and complete notification of subsidies and establish a presumption of prejudice toward subsidies not timely notified.**<sup>203</sup> Rules should further obligate a subsidizing country to prove that a given subsidy does not inflict harm on others. On the basis of these investigations, OECD nations should consider import restrictions on massively subsidized Chinese chips.

**The United States needs to dramatically expand and deepen its domestic STEM (science, technology, engineering, and mathematics) pipeline.** For instance, SIA has found that America will face a shortage of 67,000 skilled workers in the semiconductor industry by 2030 (and a gap of 1.4 million such workers throughout the broader U.S. economy).<sup>204</sup> While it’s long been a strength for America’s innovation system that many of the world’s best and brightest students have been attracted to its universities, the dependence on foreign students at the upper echelons of America’s advanced degree programs creates a significant national security vulnerability. For

instance, one 2017 study found that 81 percent of full-time graduate students in electrical engineering programs, and 79 percent in computer science, were international students.<sup>205</sup> Of those international students, a 2019 Congressional Research Service report found that nearly 70 percent of foreign students enrolled in STEM courses came from China and India.<sup>206</sup> To help build the domestic STEM talent pipeline, America should at least double its number of STEM high schools and establish an incentive program operated by the National Science Foundation to reward universities that do a better job of graduating B.S. STEM degrees.<sup>207</sup>

In its application of export controls on China, American administrations have to balance the competing interests of keeping advanced American technologies out of the national security apparatus of foreign competitors (and potentially of stymieing competitors' industrial capabilities more broadly) against the reality that export controls reduce sales for American companies, which depend on those profits to reinvest in future generations of innovation—a reality particularly pertinent when U.S. semiconductor companies rely on the Chinese market for 36 percent of their sales. Export controls have become the go-to tool of choice for U.S. policy vis-à-vis China and has come at the expense of needed U.S. sales. **As such, it's imperative that U.S. semiconductor export controls vis-à-vis China be narrowly targeted—meaning strict controls on a few products in the “small garden, high wall” formulation—or focused on true “chokepoint technologies” such as SME.**<sup>208</sup>

Moreover, **the United States should eschew the application of unilateral export controls and seek to develop a more ambitious and effective plurilateral approach to promulgate export controls among like-minded nations that have indigenous semiconductor production capacity, such as Germany, Japan, South Korea, Taiwan, the Netherlands, and the United Kingdom.**<sup>209</sup> These nations should work together to establish a common understanding of both what threats are posed to the global semiconductor industry by enterprises from non-market economies not fundamentally competing on market-based terms as well as the pace and evolution of semiconductor technology.<sup>210</sup> Then, among themselves, **these nations should establish working groups, outside the Wassenaar Arrangement (which was established in order to contribute to regional and international security and stability by promoting transparency and greater responsibility in transfers of conventional arms and dual-use goods and technologies), to develop descriptions of both the semiconductor technologies and related items that warrant controls (beyond what already exists) as well as establish common licensing policies.**

To accomplish this would require allied nations such as Japan and South Korea to significantly modernize their export control regimes, laws, and policies as well. To assist other nations in modernizing their export control regimes, **Congress should expand the remit and funding for the Export Control and Related Border Security (EXBS) Program at the U.S. Department of State.** The EXBS program works with partner governments to identify regulatory and institutional gaps and provide technical and capacity-building assistance.<sup>211</sup> While the EXBS program has traditionally been more focused on export controls around weapons of mass destruction (WMDs) and national security-related technologies, its remit could be expanded to assist allied nations in further developing their capacity to evaluate export controls on advanced technologies.

As noted, China has been committed to the development of its semiconductor industry since at least the mid-2010s, well before the Trump and then Biden administrations began to impose export controls on this sector, so it is a canard that U.S. export controls have emboldened China to embark on a crash course to develop an indigenous semiconductor industry. That said, there is

some evidence that U.S. export controls have since added urgency to Chinese efforts. In their paper, “Geopolitical Risk and Decoupling: Evidence from U.S. Export Controls” Crosignani et al. found that “by forbidding U.S. firms to export to a selected list of Chinese firms for national security reasons, export controls aim to generate a selective decoupling of U.S. firms from China.”<sup>212</sup> As they concluded:

{Export controls} prompt a supply chain reconfiguration away from Chinese customers, both those targeted by export controls and those that are not. This broad-based decoupling of U.S. firms from China is not offset by the creation of new supply chain relations in other countries. Indeed, the total number of customers declines, potentially inflicting collateral damage upon the same U.S. firms whose technology export controls are trying to protect.<sup>213</sup>

Crosignani et al.’s research shows the United States must tread very cautiously and purposefully to the extent it introduces semiconductor export controls on China.

Finally, **the United States should continue to make strong use of the Entity Listing, particularly in instances where Chinese companies are found responsible for egregious cases of IP theft.** China pilfers foreign technology by whatever means possible: The Chinese government has sponsored long-running state espionage programs targeting Western firms and research centers, and this technological espionage has carried over into cyberspace. Indeed, the acquisition of foreign semiconductor technology through IP theft has been a key pillar of Chinese strategy. For instance, in November 2018, the U.S. Department of Justice charged China’s Fujian Jinhua Integrated Circuit Co. with working to steal trade secrets from U.S. chipmaker Micron Technologies.<sup>214</sup> The incident spurred the Justice Department to launch a new initiative to combat foreign economic espionage and trade secret theft.

China’s policies to block U.S. tech companies from selling in China’s market are fundamentally antithetical to the commitments the country made in joining the WTO to conduct its economic and trade policies on a private enterprise-led, market-based, rules-governed trade in accordance with the foundational principles of national treatment, non-discrimination, and reciprocity.<sup>215</sup> To counter this, **U.S. (and other allied nation) policymakers should develop new trade tools permitting the U.S. government to reciprocally exclude Chinese competitors from competing in U.S. markets in sectors where the Chinese are denying U.S. (and other foreign) firms market access.** The European Union is already going down this path with China regarding its International Procurement Instrument (IPI), which will condition China’s access to European public procurement markets based on the extent to which China’s federal and provincial governments are permitting European firms to compete in its market.<sup>216</sup> Reciprocity should be a guiding principle of U.S. trade policy toward China.

## CONCLUSION

The Chinese government fervently wishes to transform China into a leading competitor in the global semiconductor industry—in large part to reduce foreign dependency and increase domestic self-sufficiency in this strategically crucial industry—and has committed hundreds of billions of dollars to this cause. Should it achieve this, it will impose significant damage on foreign semiconductor firms because they will lose Chinese sales. Despite the gargantuan effort, it has thus far achieved only mixed returns. Despite pockets of advancement—notably in the

design of AI chips and chips for mobile phones—overall, China likely remains about a half decade behind in its ability to design and manufacture at high volumes the most-advanced leading-edge semiconductor logic chips. As Hutcheson pointed out, “Ten years ago, [Chinese companies] were two generations behind. Five years ago, they were two generations behind, and now they’re still two generations behind.” China likely trails even more significantly in SME. To be sure, Huawei’s effort to apply double patterning (the SASQ example) shows that Chinese semiconductor developers are attempting to be as innovative as they can possibly be in process innovation, at least with the assets and technologies available, even if these are clearly behind the global frontier currently. Ultimately, Chinese enterprises are innovating as rapidly as they’re able to in this industry—and they have achieved some notable achievements and advancements—but overall, they remain behind global leaders in the semiconductor industry. But if allied nations rest on their laurels, they risk seeing a significant diminution of their industries.

## Acknowledgment

ITIF wishes to thank the Smith Richardson Foundation for supporting research on the question, “Can China Innovate?” Other reports in this series will cover artificial intelligence, biopharmaceuticals, chemicals, electric vehicles and batteries, nuclear power, quantum computing, and robotics. The author would also like to thank Ian Tufts and Robert Atkison for their assistance with this report. (Search [#ChinaInnovationSeries](#) on [itif.org](#).)

Any errors or omissions are the author’s responsibility alone.

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